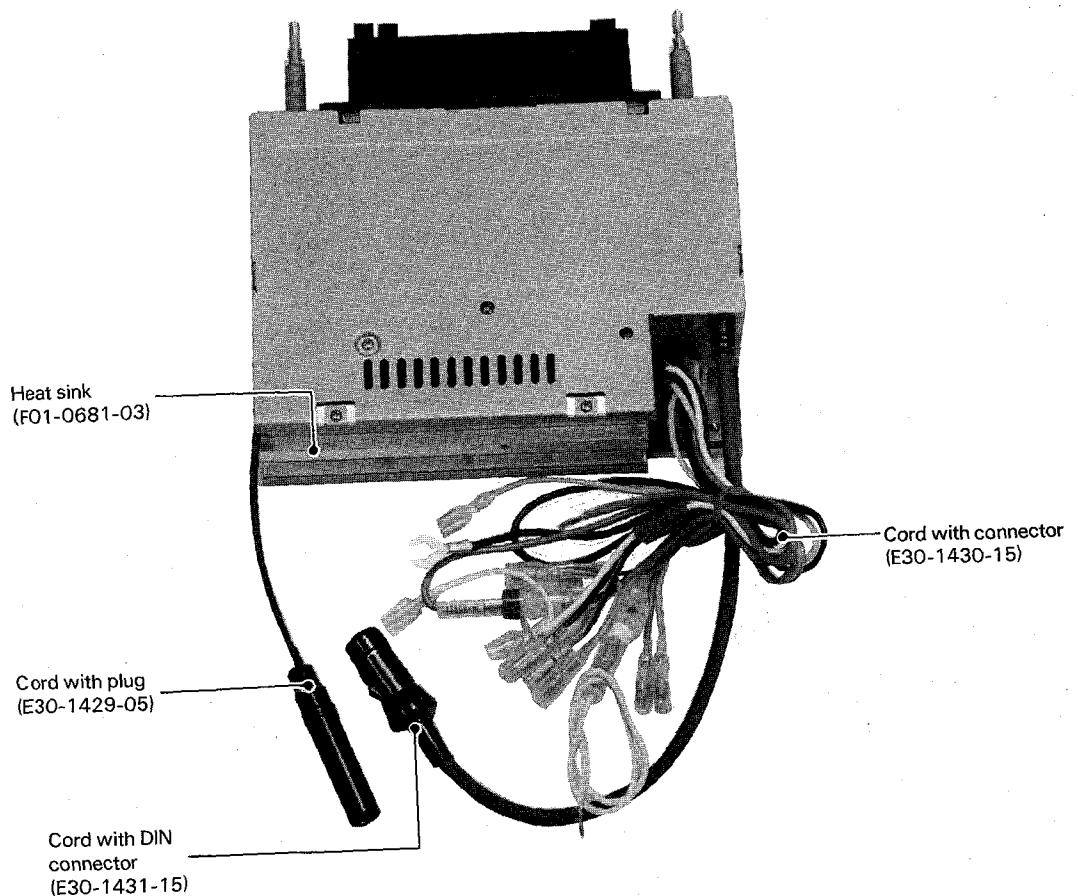
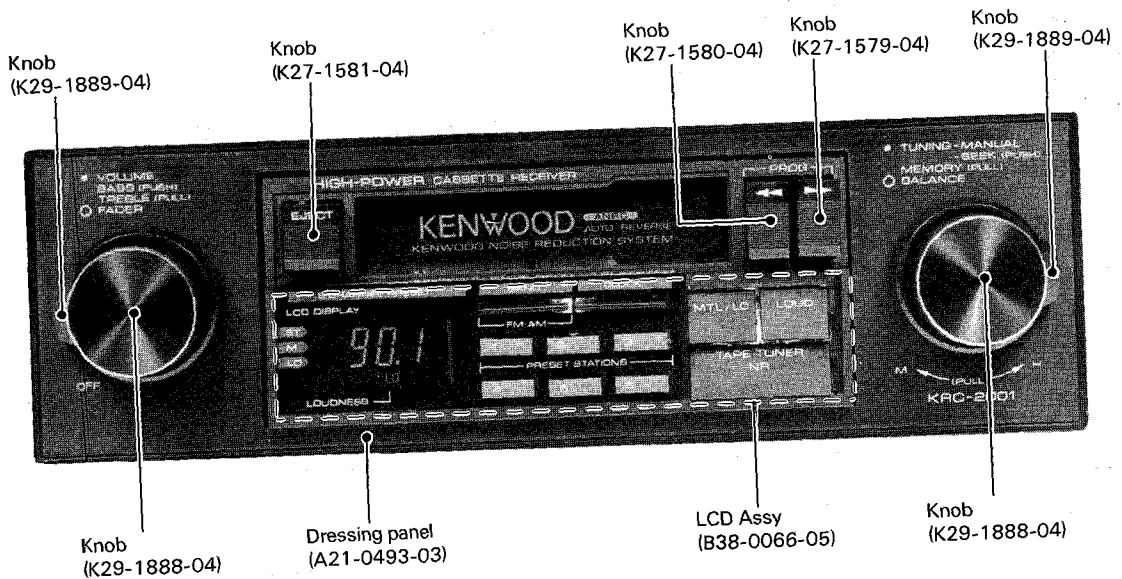


KENWOOD

KRC-2001

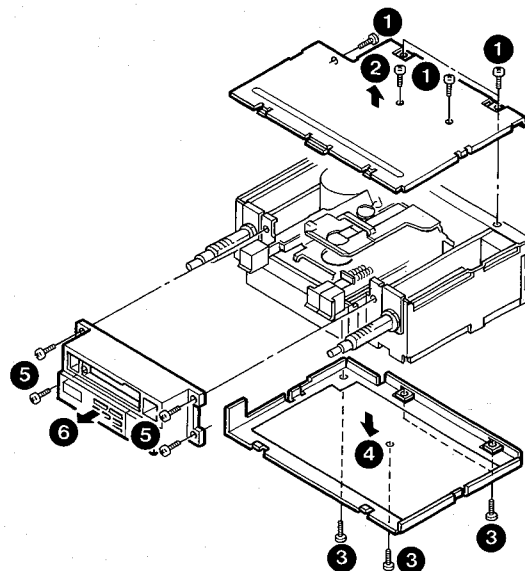
STEREO CASSETTE RECEIVER



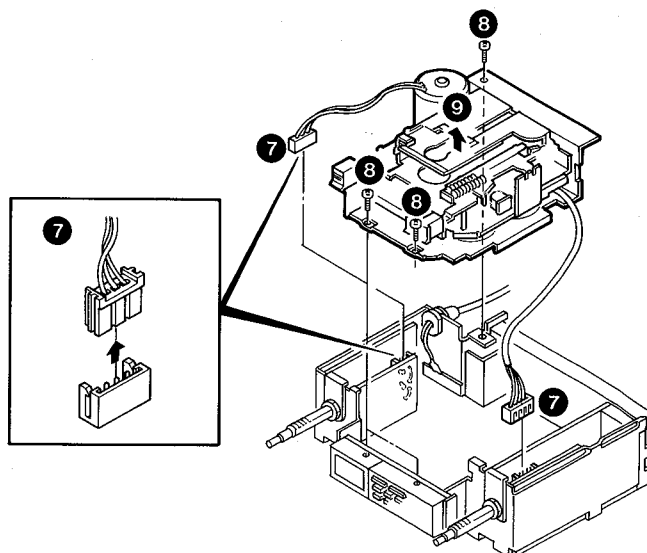
DISASSEMBLY FOR REPAIR

DISASSEMBLY FOR REPAIR

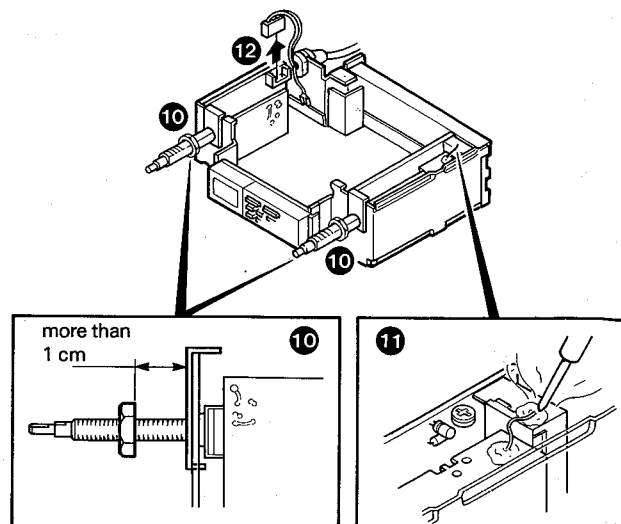
1. Remove 4 screws retaining the top cover and pull it up (1, 2)
2. Remove 4 screws and remove bottom plate (3, 4)
3. Remove 4 screws and remove the panel assy (5, 6)



4. Remove 4 screws retaining the mechanism and pull out the connectors (7, 8, 9)

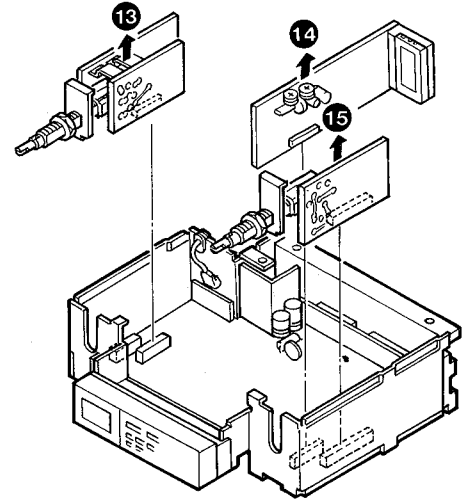


5. Remove the nuts from the axis and take off the solder from the PCB and pull out the connector (10, 11, 12)

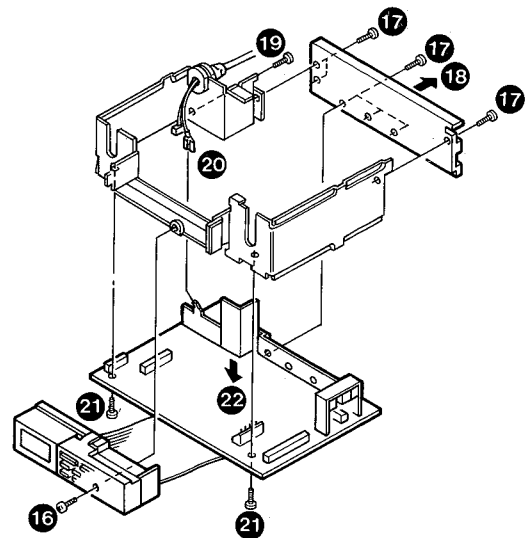


DISASSEMBLY FOR REPAIR

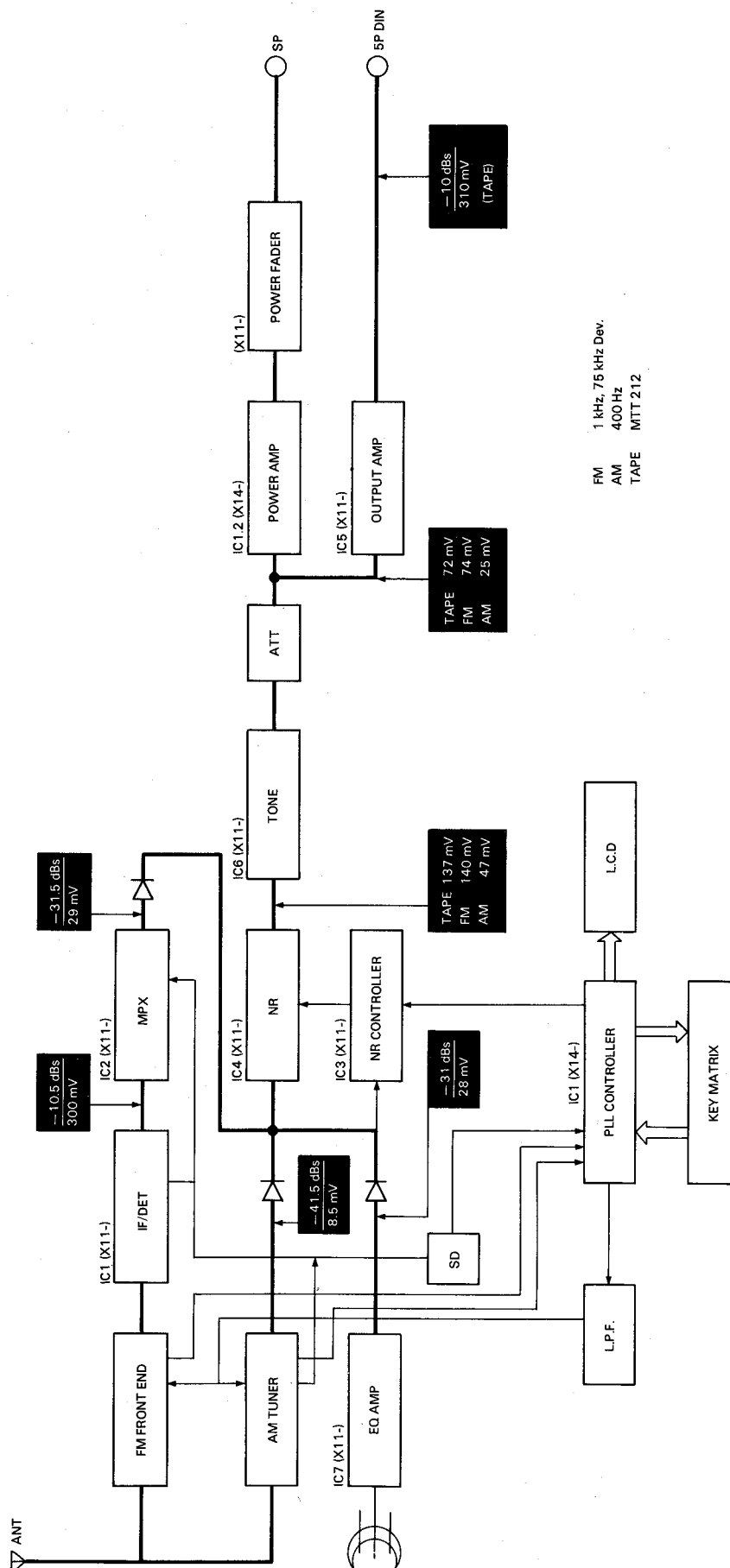
6. Pull out the PCB from the bottom main PCB (13 , 14 , 15)



7. Removes the screw of display assy (16) and remove six screws of heat sink (17 , 18)
Remove the screw (19) and pull out the connector (20)
8. Remove the screws (21) and (22)



BLOCK LEVEL DIAGRAM



CIRCUIT DESCRIPTION

Description of components.

CONTROL UNIT (X11-2230-10)

Components	Application/Function	Operation/Condition/Compatibility
Q1	LO/DX SW	
Q2	FM IF AMP	
Q3, 4	LOUDNESS SW	
IC1	FM IF AMP/DET	
IC2	FM MPX	
IC3	NR	
IC4	NR CONTROLL	
IC5	DIN OUT AMP	
IC6	TONE AMP	
IC7	EQ AMP	

SYNTHESIZER UNIT (X14-1870-10)

Components	Application/Function	Operation/Condition/Compatibility
Q1	AGC CUT	
Q2	STATION DETECT	Turned ON when the broadcast signal is received.
Q3, 4	FM BAND-WIDTH SW	Turned ON when SEEK operation (seeking)
Q5	NR SW	
Q6	METAL SW	
Q7	AM IFM SW	Turned ON when FM reception
Q8	TUNER +B SW	Turned ON when TAPE mode
Q9	FM +B SW	
Q10	AM +B SW	
Q11, 12	ACC AVR (+9 V)	
Q13	TAPE +B SW	
Q14, 15	LP.F DC AMP	
Q16	BACK UP AVR (+5 V)	
Q17	CE-CONTROL	Turned OFF when ACC is ON
Q18, 19	MUTING	
Q20	EQ MUTE DRIVER	For equalizer amp. Turned ON when Radio mode, FF/REW mode.
Q21	TAPE MUTE DRIVER	Turned ON when FF/REW mode
Q22	MUTE DRIVER	
Q23, 24	MUTE DRIVER	For power amplifier. Turned ON when ACC is ON.
Q25	AGC CUT	
IC1	MICROPROCESSOR	
IC2, 3	POWER AMP	
IC4	AND IC	(1/4) F/R indicator, (2/4) ST indicator, (3/4) STATION DETECT indicator, (4/4) TAPE MODE indicator
IC5	AND IC	(1/4) MEMORY switch, (2/4) MANUAL DOWN switch, (3/4) MANUAL UP switch, (4/4) SEEK switch

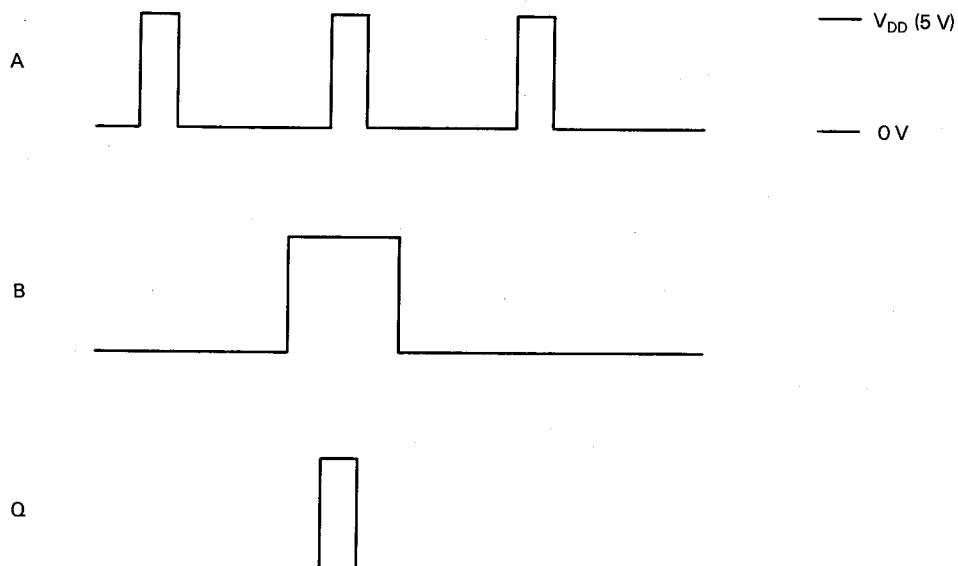
CIRCUIT DESCRIPTION

AND-GATE FOR CPU KEY MATRIX OPERATION DESCRIPTION



PIN 7 --- GND

PIN 14 --- V_{DD} (5 V)



CPU Key Matrix Operation

The source clock from the CPU is input to A-input via the AND-GATE at any time to apply the control signal to B-input.

When the signal is input to B-input, the output Q goes high and input as the CPU key input. When the B-input is low level, output Q is always low. Output Q is synchronized with input A.

CIRCUIT DESCRIPTION

Synthesizer Unit μ -Com: μ PD 1708G

FUNCTION OUTLINE

Receiving frequency, Channel spacing, Reference frequency, Intermediate frequency

FM band

Frequency range	Channel spacing	Reference frequency	Intermediate frequency
87.9 ~ 107.9 MHz	200 kHz	25 kHz	10,700
87.50 ~ 108.0 MHz	* 50 kHz	12.5 kHz	10,700

* MANUAL 25 kHz

AM band

Frequency range	Channel spacing	Reference frequency	Intermediate frequency
530 ~ 1620 kHz	10 kHz	10 kHz	450 kHz
522 ~ 1611 kHz	9 kHz	9 kHz	450 kHz
153 ~ 281 kHz	* 9 kHz	1 kHz	450 kHz

* MANUAL 1 kHz

Tuning Function

- (1) Auto Tuning (Sawtooth wave mode)
Seek Up: Once a station is tuned, it is held tuned.
- (2) Manual Tuning (Sawtooth wave mode)
Manual Up/Down: Frequency is advanced up or down in steps by pressing the push switch.
Pressing for a half second or more advances it up or down continuously until the switch is released.
- (3) Preset Memory Recall
6 stations on each FM, MW, and LW band can be preset independently with the 6 buttons. The last station is stored in memory for each band when power is turned off.

Tape Function

- (1) Tape running indicator
- (2) METAL control
- (3) DOLBY control

Radio Function

- (1) LOC (local) control
- (2) MONO control

Clock Function

- (1) 12-hour display

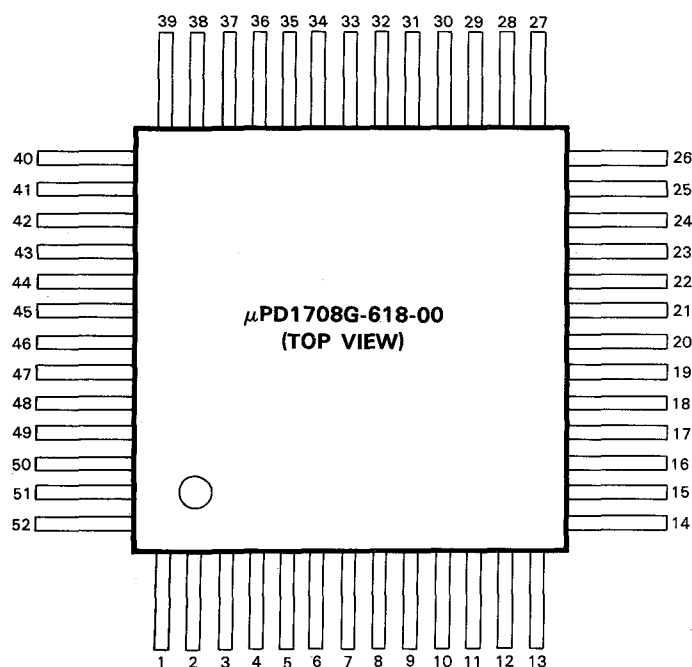
Other Functions

- (1) LOUDNESS control
- (2) NR (noise reduction) control

CIRCUIT DESCRIPTION

TERMINAL DESCRIPTION

Terminal Configuration (Top View)



Pin No.	Pin Name	Pin No.	Pin Name
1	LCD4	27	KS ₁ (PB ₁)
2	LCD3	28	KS ₀ (PB ₀)
3	LCD2	29	BANDZ/N.R
4	LCD1	30	METAL-LOC
5	COM2	31	LOUDNESS
6	COM1	32	*
7	V _{DD}	33	*
8	FM	34	*
9	AM	35	*
10	GND	36	*
11	EO ₁	37	*
12	EO ₂	38	LCD19
13	CE	39	LCD18
14	*	40	LCD17
15	XI	41	LCD16
16	XO	42	LCD15
17	AF MUTE (PA ₃)	43	LCD14
18	BAND 1 (PA ₂)	44	LCD13
19	KS ₅ /K ₅ (PA ₁)	45	LCD12
20	KS ₄ /K ₄ (PA ₁)	46	LCD11
21	K ₃	47	LCD10
22	K ₂	48	LCD9
23	K ₁	49	LCD8
24	K ₀	50	LCD7
25	KS ₃ (PB ₃)	51	LCD6
26	KS ₂ (PB ₂)	52	LCD5

* Not used.

CIRCUIT DESCRIPTION

Pin description

Pin No.	Symbol	Pin Name	Description
1~4 34~52	LCD1 } LCD23	LCD segment signal	LCD segment signal output pin (1/2 duty, 1/2 bias LCD should be used. Frame frequency: 100 Hz, Drive voltage: VDD)
5 6	COM2 COM1	LCD common signal	LCD common signal output pin
7 33	V _{DD}	Power input	Device power supply pins During device operation, 5 V \pm 10% voltage is supplied via these pins. Either of them can be used for supplying the power individually. The rising time of VDD should be less than 500 ms (0 to 4.5 V). When the rising time is too long, or when the VDD is not lowered completely to 0 V and then raised to 4.5 V from the voltage lower than the operating rate, the diode switch condition for initialization is not read out correctly. In such cases, use the CE pin so that the diode switch status can be read out for initialization.
8	FM	FM VCO input	This pin inputs the FM station output signal. Since it incorporates the AC amp, cut the DC signal with the capacitor.
9	AM	AM VCO input	This pin inputs the AM station output signal. Since it incorporates the AC amp, cut the DC signal with the capacitor.
10	GND	Ground	Connect to the ground terminal of the set.
11	EO ₁	Error Out	Charge pump output of the phase detector consisting of PLL. When the frequency divided by the oscillating frequency is higher than the reference frequency, these pins output high level signals, and when it is lower than the reference frequency, they go low. When the frequency (divided by the oscillating frequency) is coincided with the reference frequency, it enters into the floating status.
13	CE	Chip Enable	This pin is used to input the selected signal from the device. When operating the PLL section, this pin goes high, and when the PLL section is stopped, it goes low. When at low level, the display goes off. However, a low level signal below 134 μ s or high level signal is not accepted.
15 16	XI XO	Crystal resonator	Connectors of the crystal resonator. Connect the 4.5 MHz crystal resonator.
17	AF MUTE	Mute Out	This pin outputs the muting signal to eliminate shock noise when the PLL is unlocked and pop noise when switching between Tape and Radio, and is active low. (CMOS output) For timing details, refer to the AF Mute Out Timing Chart. When the CE pin is low, this pin is active low.
18	BAND ₁	Band Out	FM/MW switching output pin FM: High MW: Low When the MODE switch is set to "1" (Tape mode), this pin is low. When the SDK is provided, follow the SDK section.
19	KS ₅ /K ₅	Key return signal source and Key return signal input	This becomes the source of key return signal to read out the diode matrix for initialization only when the power is turned on for the first time (rising time of VDD) or when the set is returned from the back-up condition (CE: Low to High). Then, this inputs the key return signal for the key matrix. Insert the pull-down resistor. (CMOS input/output)
20	KS ₄ /K ₄	Key return signal source and Key return signal input	This becomes the source of the key return signal to read out the diode matrix for initialization only when the power is turned on for the first time (VDD rising time) or when returning from the back-up condition (CE goes high from low). Then, this inputs the key return signal for the key matrix. Insert the pull-down resistor. (CMOS input/output)
21 24	K ₃ } K ₀	Key return signal input	This pin inputs the key return signal for the key matrix. Insert the pull-down resistor. (CMOS input)
25 28	KS ₃ } KS ₀	Key return signal source	This pin outputs the key return signal for the key matrix. Since the synchronous current is greatly lowered because of its configuration, the reverse-current prevention diode will be not necessary for the key source side. (CMOS output)
30	METAL/DX/LOC	LOC Out	In radio mode: DX/Local On/Off output pin When "LOC" is displayed on the LCD panel, high level signal is output. When it is not lit, low level signal is output. (When the power is turned on, low level status is initialized.) In tape mode: METAL On/Off output pin When "METAL" is displayed on the LCD panel, low level signal is output. When it is not lit, high level signal is output. On initialization when the tape power is turned on, high level is output.

CIRCUIT DESCRIPTION

Pin No.	Symbol	Pin Name	Description
31	LOUDNESS	Loudness Out	LOUDNESS output pin When "LOUD" is displayed on the LCD panel, low level signal is output. When it is not lit, high level signal is output. When the power is turned on first (VDD rising time), low level signal is output. (CMOS output)
32			DOLBY output pin When "DOLBY" is displayed on the LCD panel, high level signal is output. When it is not lit, low level signal is output. On initialization when the power is turned on, low level is output.

BAND2/NR

When Band A is "0" or "1" and the NR selector is "1", this functions as the NR on/off output pin. When "NR" is displayed on the LCD panel, high level signal is output. When it is not lit, low level signal is output.

This pin can be operated in the TAPE/RADIO mode.

On initialization when the power is turned on, this pin is at low level.

When BAND A is "0", "1" and the NR selector is "0", this function as the WIDE-ADV on/off output pin.

- In the Radio mode:**

This functions as the WIDE on/off output pin.

When "WIDE" is displayed on the LCD panel, high level signal is output, and when it is not lit, low level is output.

- In the Tape mode:**

This functions as the ADV on/off output pin.

When "ADV" is displayed on the LCD panel, high level signal is output, while it is not lit, low level is output.

On initialization when the power is turned on first, it is at low level.

When BAND A is "0" and the NR selector is "0" (SDK operation is normal only when in this status), and BAND B is "1", this pin functions as the BAND 2 output. BAND 2 becomes the band switching output port in combination with BAND 1.

Mode	Output	BAND 1	BAND 2
MW		L	L
FM		H	L
LW		L	H

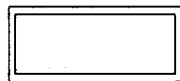
CIRCUIT DESCRIPTION

1. KEY MATRIX CONFIGURATION

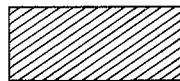
1-1. Key Matrix Layout

Input pin Output pin	K ₅ (19)	K ₄ (20)	K ₃ (21)	K ₂ (22)	K ₁ (23)	K ₀ (24)
KS ₀ (28)		SEEK UP	NR	LOUDNESS	DX/LOC-MTL	MONO-DOLBY
KS ₁ (27)	MD	MU	M4	M3	M2	M1
KS ₂ (26)	ME		M6	M5	RCAL	BAND
KS ₃ (25)			MODE	SD	ST	FOW/REV
KS ₄ (20)			CLK/FREQ	NR SEL	BAND B	
KS ₅ (19)			BAND A	PRIORITY	BAND C	CLKSEL

The number in the bracket shows the pin no.



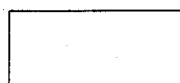
: Momentary switch



: Alternate switch or transistor switch

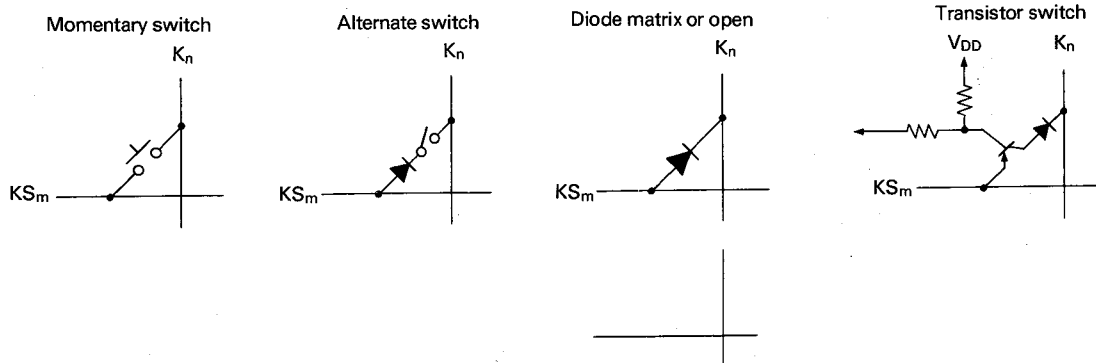


: Diode matrix (closed/opened by diode)



: Open

1-2. Switch Connection



CIRCUIT DESCRIPTION

1-3. Key Matrix Connection



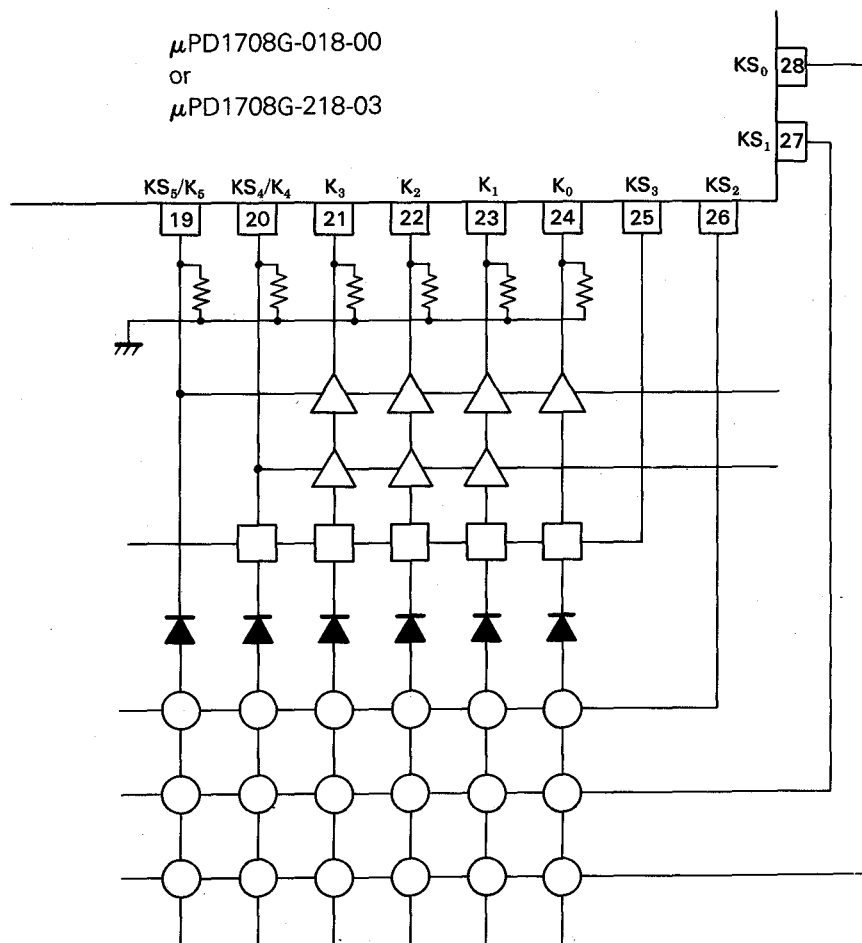
: Momentary switch



: Alternate switch or transistor switch



: Diode matrix



CIRCUIT DESCRIPTION

2. KEY MATRIX DESCRIPTION

2-1. Diode Matrix for Initialization

The diode matrix for initialization has the following five status. All status is read out only when the power is supplied to the V_{DD} for the first time (Power-ON, Reset) and when the CE pin goes high from low level (CE Reset), in another periods, the diode matrix status is ignored.

- (1) The switch for setting the receiving frequency range and the channel spacing:

BAND A

- (2) Clock signal select switch:

CLKSEL

- (3) Priority select switch for display:

PRIORITY

- (4) —

- (5) NR select switch:

NR SEL

- (6) CLOCK/FREQUENCY select switch:

CLOCK/FRQ

- (7) LW select switch:

BAND B

Symbol	Function Description																				
BAND A	<p>This switch is used for setting the receiving frequency range for each FM/MW/LW band channel spacing. Each setting status is as follows:</p> <table><tr><th>BAND A</th><th>Frequency Range</th><th>Channel Spacing</th><th>Manual Step</th></tr><tr><td>1</td><td>87.9~107.9 MHz</td><td>200 kHz</td><td>—</td></tr><tr><td>1</td><td>530~1620 kHz</td><td>10 kHz</td><td>—</td></tr><tr><td>0</td><td>87.5~108.0 MHz</td><td>50 kHz</td><td>25 kHz</td></tr><tr><td>0</td><td>522~1611 kHz</td><td>9 kHz</td><td>—</td></tr></table>	BAND A	Frequency Range	Channel Spacing	Manual Step	1	87.9~107.9 MHz	200 kHz	—	1	530~1620 kHz	10 kHz	—	0	87.5~108.0 MHz	50 kHz	25 kHz	0	522~1611 kHz	9 kHz	—
BAND A	Frequency Range	Channel Spacing	Manual Step																		
1	87.9~107.9 MHz	200 kHz	—																		
1	530~1620 kHz	10 kHz	—																		
0	87.5~108.0 MHz	50 kHz	25 kHz																		
0	522~1611 kHz	9 kHz	—																		
PRIORITY	<p>When the clock function is provided, this switch selects the priority mode for display. When the display which does not have priority is recalled, the mode having priority will be displayed after approx. 5 seconds. "1": No priority "0": Priority is provided.</p>																				
CLKSEL	<p>Select switch to provide the clock function or not. "1": Clock is not provided (For back-up, RAM is not cleared) "0": Clock is provided.</p>																				
NR SEL	<p>Select switch to provide the NR (noise reduction) function. "0": NR is not provided (WIDE-ADV and BAND 2 output pin) "1": NR provided</p>																				
CLOCK/FRQ	<p>Select switch to provide priority to the clock or frequency for display (Depending on PRIORITY) "0": Frequency "1": Clock</p>																				
BAND C	<p>Switch to access the preset memory (M1 to M6) sequentially "0": M1 to M6 keys are preset independently "1": Each time the M1 key is pressed, preset memory is sent sequentially</p> <div>→M₁-----M₆←</div>																				

CIRCUIT DESCRIPTION

2-2. Mode Select Switches

Unlike the initializing switches, these switches can be changed at any times. (On the following table, "1" shows switched ON, "0" shows switched OFF.)

Symbol	Function Description
MODE	Set the unit to RADIO mode or TAPE mode. "1": TAPE mode "0": RADIO mode
SD	In the RADIO mode: This is the Station Detector input in SEEK or SCAN mode. This should be set to OFF within approx. 50 ms after the PLL is locked. When every times are OFF by detecting the station every 1 ms, the station is recognized as received and the seeking or scanning operation stops.
STEREO	In the RADIO mode: (Only for FM reception) Stereo signal input switch. When this switch turns OFF, "ST" is displayed on the LCD panel. However, "ST" goes off in the Auto Tuning mode (AF-MUTE pin is active) even if this switch is OFF.
FOW/REV	In the Tape mode: Tape running direction indicator input switch. When this switch turns ON, the "REV" (◀) is displayed on the LCD panel. When it turns OFF, the "FOW" (▶) is displayed. This switch functions only when the CE pin is high and the MODE switch is "ON" (Tape mode).

2-3. Momentary Switches

Symbol	Function Description																																																												
MU MD	<p>These keys are used for manual tuning and time adjustment.</p> <ul style="list-style-type: none">Frequency display Each time the key is pressed, the displayed frequency is advanced up (by MU key) or down (by MD key) by 1 step (channel spacing set). When it is pressed for a half second or more, the frequency is advanced rapidly (continuously) until it is released.Clock (time) display While pressing the ME key, press the MD key to adjust the time, and press the MU key to adjust minutes.																																																												
M1 } M6	<p>In the Radio mode: These keys are used to write or recall the preset memory. FM, MW and LW bands can be stored independently into each key in memory.</p> <p>(1) When writing With the frequency display, within five seconds after pressing the ME key, press one key (M1 to M6), to store the frequency currently received into memory.</p> <p>(2) When recalling When one key (M1 to M6) is pressed, the memory content (frequency) corresponding to the key pressed is recalled. When the radio is turned on after the VDD is first turned on, the lowest frequency on the FM band is recalled. When shipped, the following frequencies are preset into M1 to M6 key for adjustment at the factory.</p> <table><tr><th colspan="2">Preset Memory Key</th><th>M1</th><th>M2</th><th>M3</th><th>M4</th><th>M5</th><th>M6</th></tr><tr><th>Band</th><th>Frequency Range</th><th></th><th></th><th></th><th></th><th></th><th></th></tr><tr><td rowspan="4">FM</td><td>87.9~107.9 MHz</td><td>87.9</td><td>90.1</td><td>98.1</td><td>106.1</td><td>107.9</td><td>87.9</td></tr><tr><td>87.50~108.00 MHz</td><td>87.50</td><td>90.1</td><td>98.1</td><td>106.1</td><td>108.00</td><td>87.50</td></tr><tr><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr><tr><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr><tr><td rowspan="2">MW</td><td>530~1620 kHz</td><td>530</td><td>600</td><td>1000</td><td>1400</td><td>1620</td><td>530</td></tr><tr><td>522~1611 kHz</td><td>522</td><td>603</td><td>999</td><td>1404</td><td>1611</td><td>522</td></tr></table>	Preset Memory Key		M1	M2	M3	M4	M5	M6	Band	Frequency Range							FM	87.9~107.9 MHz	87.9	90.1	98.1	106.1	107.9	87.9	87.50~108.00 MHz	87.50	90.1	98.1	106.1	108.00	87.50															MW	530~1620 kHz	530	600	1000	1400	1620	530	522~1611 kHz	522	603	999	1404	1611	522
Preset Memory Key		M1	M2	M3	M4	M5	M6																																																						
Band	Frequency Range																																																												
FM	87.9~107.9 MHz	87.9	90.1	98.1	106.1	107.9	87.9																																																						
	87.50~108.00 MHz	87.50	90.1	98.1	106.1	108.00	87.50																																																						
MW	530~1620 kHz	530	600	1000	1400	1620	530																																																						
	522~1611 kHz	522	603	999	1404	1611	522																																																						

CIRCUIT DESCRIPTION

Symbol	Function Description
M1 } M6	<p>These keys are used to write and recall the preset memory.</p> <p>Each FM, MW and LW frequency can be stored into one key in memory independently.</p> <p>However, the number of available bands differ with the area designated by the initializing diode matrix, as follows:</p> <p>For the area only 2 bands are available: 6 stations \times 2 = 12 stations</p> <p>For the area 3 bands are available: 6 stations \times 3 = 18 stations</p> <p>Corresponding to the preset key pressed, the "CH" indicator and " " (channel number) are displayed on the LCD panel.</p>
SEEK UP/DOWN	<p>These keys are used for automatic tuning. During auto tuning operation, when the SD switch is turned OFF, the frequency displayed at the time is kept on hold.</p> <p>In auto tuning mode, the auto tuning operation is continued even when the LOUDNESS, ME, NR, METAL-DX/LOC, or MONO-DOLBY key is pressed.</p> <p>When one of the other keys is pressed, the auto tuning operation is stopped, and the unit enters the operation of the key pressed.</p> <p>When the SEEK key is pressed again, the frequency before the SEEK operation is resumed.</p>
DX/LOC —MTL	<p>This key is used to select the function between DX/LOC — MTL.</p> <ul style="list-style-type: none"> In the Radio mode: Each time the key is pressed, the LOC output pin and the "LOC" display on the LCD panel are inverted. When the "LOC" display on the LCD panel, high level signal is output from the LOC Out pin, and when it is not lit, low level is output. In the Tape mode: Each time the key is pressed, the LOC output pin and the "MTL" display are inverted. When the "MTL" is displayed on the LCD panel, low level signal is output from the LCD Out pin, and when it is not lit, high level is output. <p>By initialization when the power is turned on, high level signal is output.</p>
ME	<p>This key is used for writing the preset memory. It is also used for adjusting the time on clock display.</p> <ul style="list-style-type: none"> Frequency display: Used when writing a new frequency into the preset memory. When this key is pressed, the "ME" is displayed on the LCD panel, and lit for five seconds after the key is released. While the "ME" is lit, pressing one key (M1 to M6) stores the displayed frequency into memory corresponding to the key pressed. To cancel the preset memory, while the "ME" is lit, press any key other than ME, NR, METAL-DX/LOC, MONO-DOLBY, or LOUDNESS. Clock display: The "hour" and "minutes" can be adjusted by pressing the MD or MU key while pressing the ME key. After pressing the ME key, each time the MD key is pressed, the "hour" is advanced one by one. Pressing it for a half second or more advances the time by 4 hours/sec continuously until the MD key is released. This operation does not affect the "minute" or "second" digits (they are not displayed during this operation). After pressing the ME key, each time the MU key is pressed, the "minute" is advanced one by one. Press it for a half second or more advances the minute in 8 minutes/sec speeds continuously, until the MU key is released. The "second" is not displayed, however, it is reset to zero every time the "minute" is set. The "minute" adjusting does not affect the "hour". ("Hour" is not changed even when the "minute" exceeds 60.) (During clock display, pressing the ME key alone changes the display to frequency and "ME" is displayed. In this condition, pressing one of the preset keys (M1 to M6) stores the frequency into the memory corresponding to the key pressed.)
BAND	<p>This key is used to select the band.</p> <p>When Band A is "0" or "1" and Band B is "0" (LW: Not available) ...</p> <p>Each time this key is pressed, the band is changed in the order of FM — MW — FM ...</p>
LOUDNESS	<p>Used for Loudness select key.</p> <p>Each time this key is pressed, the loudness output pin and the "LOUD" display on the LCD panel are inverted. When the "LOUD" is displayed on the LCD panel, low level signal is output from the Loudness pin and when it is not lit, high level is output.</p> <p>By initialization when the power is first turned on (rising time of VDD), "LOUD" is displayed and low level is output.</p>

CIRCUIT DESCRIPTION

Symbol	Function Description
RCAL	<p>Display select key. Available only when in the radio mode.</p> <p>When this key is pressed, the display is changed from the clock display to frequency or vice versa. However, five seconds after the key is pressed, the display is restored to the priority mode (depending on the diode matrix PRIORITY).</p> <p>When the clock is not provided (CLKSEL=0), this key has no effect.</p> <p>However, the clock display is resumed by the PRIORITY switch when the display priority is provided.</p> <p>a) ON: Priority is provided b) OFF: No priority</p>
N.R	<p>(1) NR key (RADIO/TAPE common key) (2) WIDE-ADV key (Independent RADIO/TAPE key)</p> <p>(1) NR key: BAND A: "0", "1" NR SEL: "1" With the above status, this key is used as the NR select key. Each time the key is pressed, the BAND2/NR output pin and "NR" display on the LCD panel are inverted. When "NR" is displayed on the LCD panel, the BAND2/NR pin outputs the high level, and when the display is not lit, low level is output. (By initialization when the power is turned on, it outputs low level.)</p> <p>(2) WIDE-ADV key: BAND A: "0", "1" NR SEL: "1" With the above status, this key is used as the WIDE-ADV select key.</p> <p>In the Radio mode: Used as the WIDE select key. Each time the key is pressed, the BAND2/ADV output pin and the "WIDE" display on the LCD panel are inverted. When the "WIDE" is displayed on the LCD panel, the BAND2/ADV pin outputs the high level, and when the display is not lit, low level is output.</p> <p>On Tape mode: Used as the ADV select key. Each time the key is pressed, the BAND2/NR output pin and the "ADV" display on the LCD panel are inverted. When the "ADV" is displayed on the LCD panel, the BAND2/NR pin outputs the high level. When the display is not lit, the low level is output. (By initialization when the power is turned on, low level is output.)</p> <p>Note: When the following status is selected in the diode matrix, the NR key and the WIDE-ADV key are not effective. NR SEL: "0" BAND A: "0" BAND B: "1"</p>
M5 BAND	<p>This key is used for setting the received frequency range for FM/MW/LW band and the channel spacing.</p> <ol style="list-style-type: none"> By initialization when the power is turned on, the receiving frequency and channel spacing are registered by the diode of BAND A. Then, when the CE pin goes $\overline{L} \rightarrow \overline{H}$ or vice versa, they follow the diode of BAND A. When the CE pin is inverted to high from low while pressing the M5 key and the BAND key together, the band setting of BAND A is changed from "1" to "0" or from "0" to "1". Then, when the CE pin is inverted to $\overline{L} \rightarrow \overline{H}$ or vice versa, the changed area setting is maintained. When the CE pin is inverted from low to high while pressing the M5 key and BAND key together, the band setting follows the diode of BAND A. Then, when the CE pin is inverted from $\overline{L} \rightarrow \overline{H}$ or vice versa, it follows the diode of BAND A, too. To change the setting by the M5 key and the BAND key, repeat procedure 2 and 3. <p>Note: On initialization when the power is turned on, the M5 key and the BAND key are ignored even when they are pressed, and the setting is followed to the diode.</p>

ADJUSTMENT

Set the controls and switches as follows.

BALANCE	:center position	LOUD	:OFF	LOCAL	:OFF
FADER	:center position	T-ADV	:OFF	AUTO	:OFF
BASS	:center position	METAL	:OFF		
TREBLE	:center position	DOLBY NR	:OFF		

No.	ITEM	INPUT SETTINGS	OUTPUT SETTINGS	TUNER(RECEIVER) SETTINGS	ALIGNMENT POINTS	ALIGN FOR	FIG.
FM SECTION							
1	DISCRIMINATOR	(A) 98.1MHz 0 dev 60dB(ANT input)	Connect the DC voltmeter between pins of TP1	FM 98.1MHz	T1 (X11)	0V	(a)
2	VCO	(A) 98.1MHz 0 dev 60dB(ANT input)	Connect the frequency counter pins of TP2	FM 98.1MHz	VR5 (X11)	19.00kHz	(b)
3	SEPARATION	(C) 98.1MHz 1kHz, ±67.5kHz dev Selector:L or R 60dB(ANT input)	(B)	FM 98.1MHz	VR4 (X11)	Adjust it so that the crosstalk from L to R and R to L become minimum.	(c)
4	STOP LEVEL	(A) 98.1MHz 0 dev 20dB(ANT input)	—	FM 98.1MHz	VR3 (X11)	STOP	(d)
5	SOFT MUTE (1)	(A) 98.1MHz 1kHz, ±75kHz dev 60dB(ANT input)	(B)	FM 98.1kHz	—	Set the volume to 0 dBs.	
6	SOFT MUTE (2)	ANT OPEN (No signal)	(B)	FM 98.1kHz	VR1	— 25dBs	(f)
AM SECTION							
(1)	STOP LEVEL	(D) 990kHz 400Hz, 30% mod 38dB(ANT input)	—	AM 990kHz	VR1 (X14)	STOP	(g)
CASSETTE DECK SECTION							
[1]	AZIMUTH	MTT-114(10kHz)	(B)	TAPE PLAY	Head Azimuth Screw	Adjust the azimuth for each L-CH/R-CH or FOW/REV becomes maximum.	(b)

REGLAGE

Régler les contrôles et les boutons comme suit.

BALANCE : position centre LOUD : OFF LOCAL : OFF
 FADER : position centre T • ADV : OFF AUTO : OFF
 BASS : position centre METAL : OFF
 TREBLE : position centre DOLBY NR : OFF

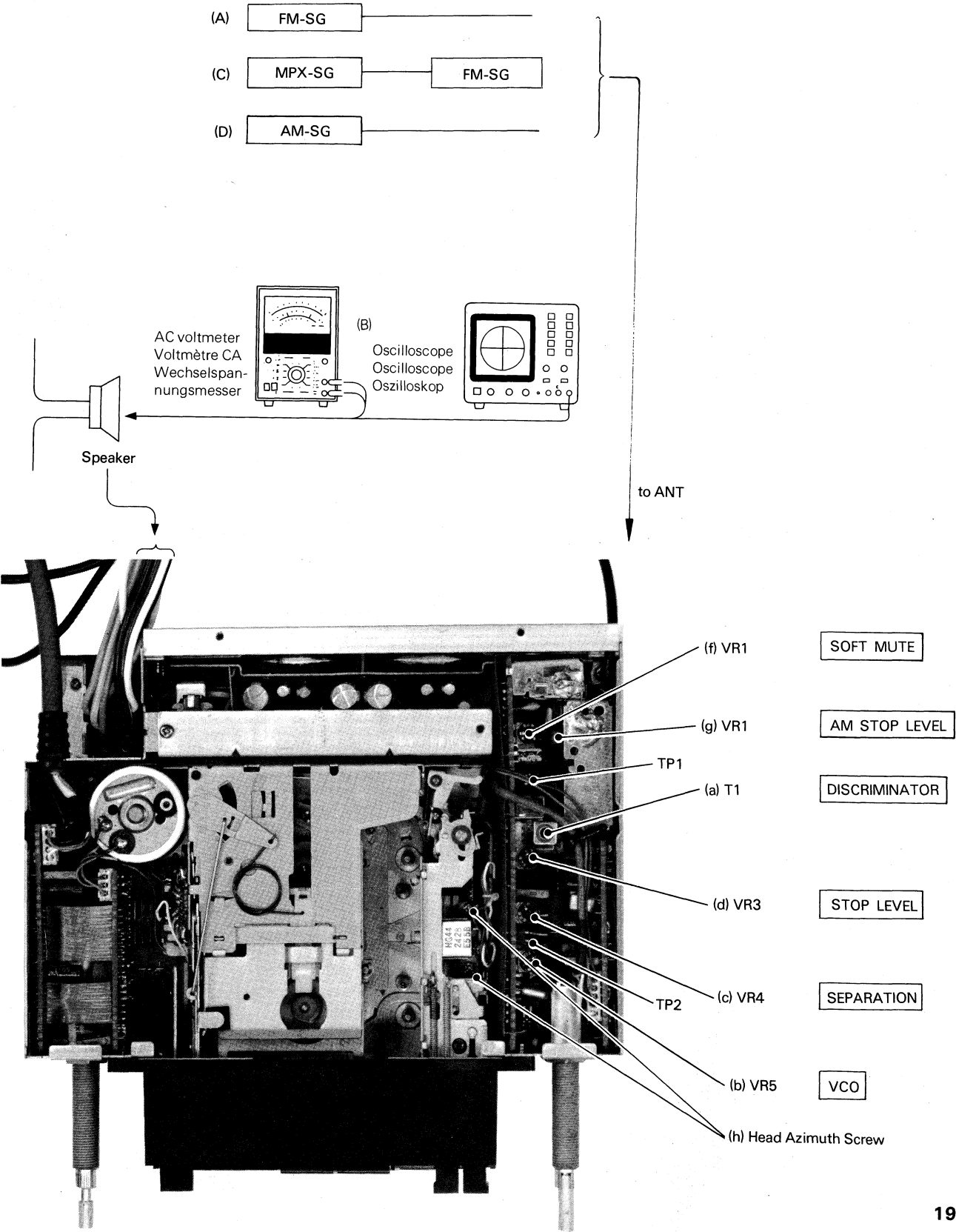
N°	ITEM	REGLAGE DE L'ENTREE	REGLAGE DE LA SORTIE	REGLAGE DU TUNER (AMPLI-TUNER)	POINTS DE L'ALIGNEMENT	ALIGNER POUR	FIG.
SECTION MF							
1	DISCRIMINATEUR	(A) 98,1MHz 0 dev 60dB(Entrée ANT)	Raccorder le voltmètre CC entre les deux broches de TP1	FM 98,1MHz	T1 (X11)	0V	(a)
2	VCO	(A) 98,1MHz 0 dev 60dB(Entrée ANT)	Raccorder le compteur de fréquence aux broches de TP2	FM 98,1MHz	VR5 (X11)	19,00kHz	(b)
3	SEPARATION	(C) 98,1MHz 1kHz.±67,5kHz dev Selecteur:L ou R 60dB(Entrée ANT)	(B)	FM 98,1MHz	VR4 (X11)	Le régler de manière à ce que la diaphonie de L à R et de R à L devienne minimum.	(c)
4	NIVEAU D'ARRET	(A) 98,1MHz 0 dev 20dB(Entrée ANT)	—	FM 98,1MHz	VR3 (X11)	ARRET	(d)
5	SILENCIEUX DOUX (1)	(A) 98,1MHz 1kHz.±75kHz dev 60dB(Entrée ANT)	(B)	FM 98,1MHz	—	Régler le volume à 0 dBs.	
6	SILENCIEUX DOUX (2)	Ouvrir l'antenne (pas de signal)	(B)	FM 98,1MHz	VR1	— 25dBs.	(f)
SECTION MA							
(1)	NIVEAU D'ARRET	(D) 990kHz 400Hz. 30% mod 38dB(Entrée ANT)	—	AM 990kHz	VR1 (X14)	ARRET	(g)
SECTION DU MAGNETPHONE							
[1]	AZIMUTH	MTT-114 (10kHz)	(B)	Lecture bande	Vis d'azimut de tête	Ajuster l'azimut pour que chaque L-CH/R-CH ou FOW/REV devienne maximum.	(b)

ABGLEICH

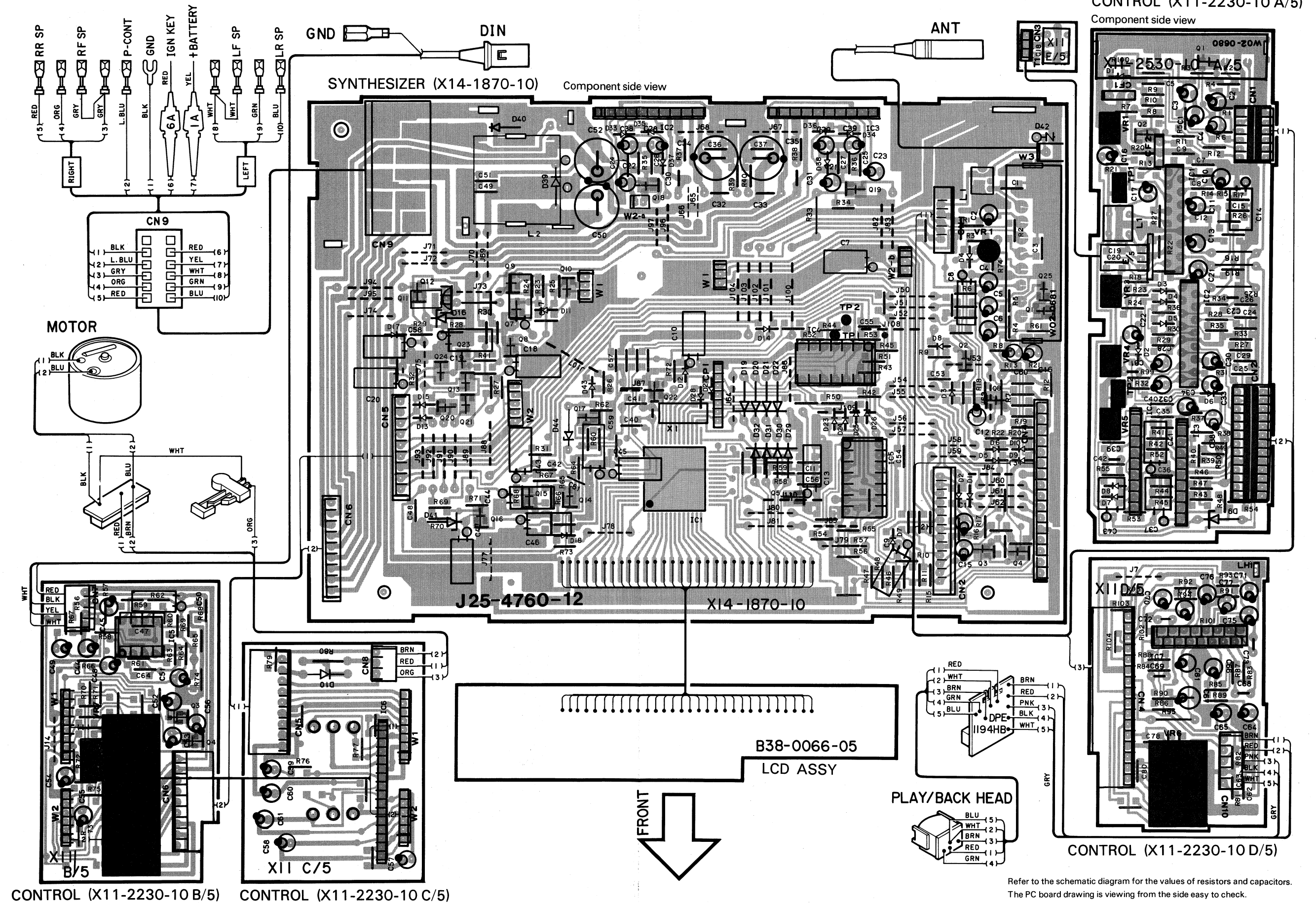
ADJUSTMENT/REGLAGE/ABGLEICH

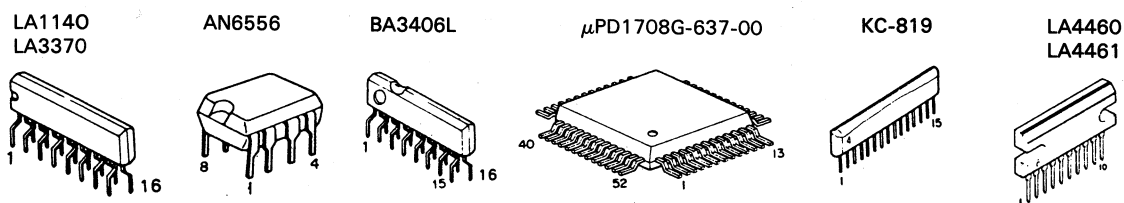
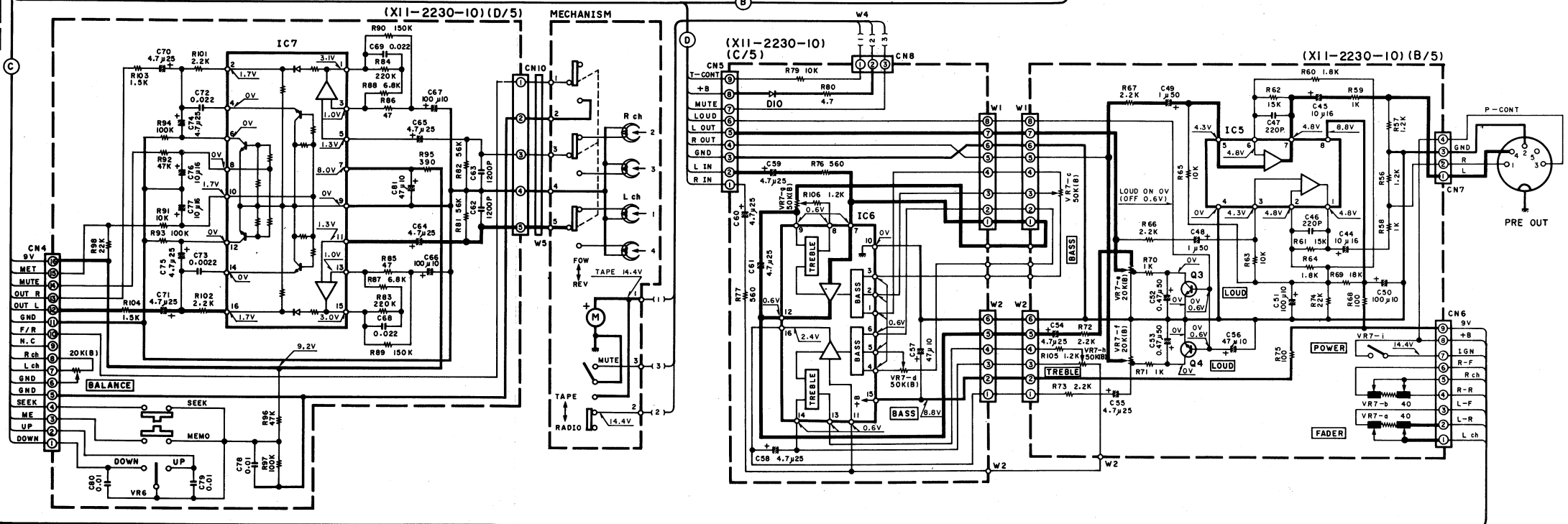
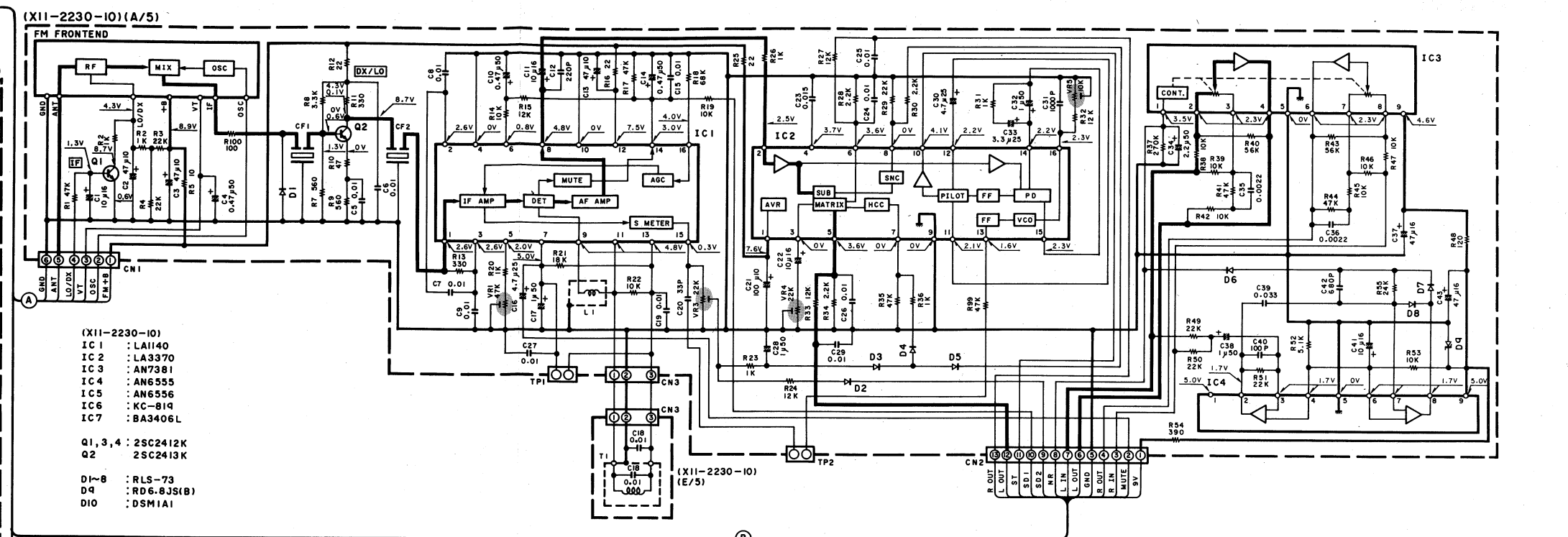
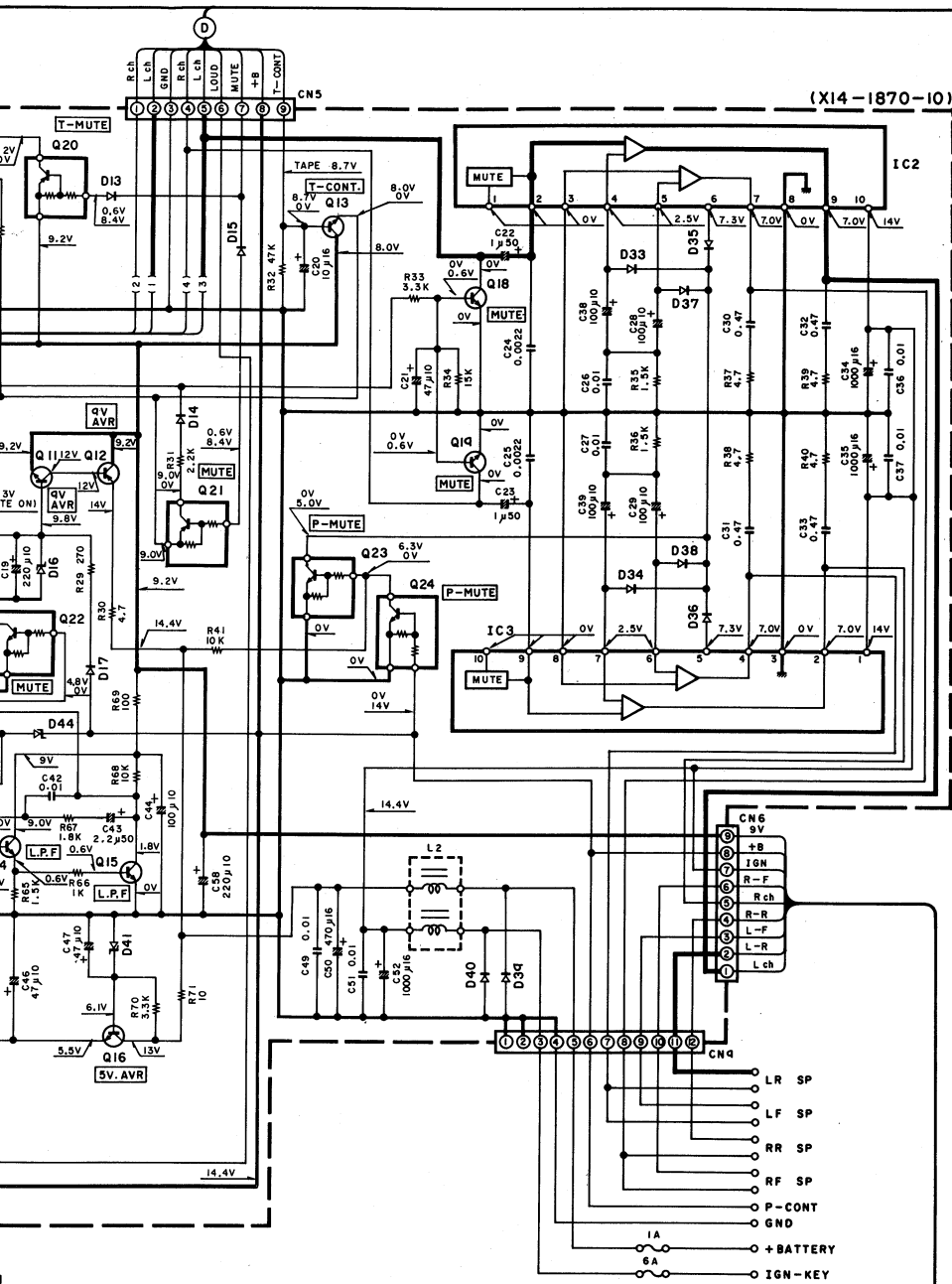
Die Regler und Knöpfe wie folgt einstellen.
BALANCE :Mittelage LOUD :OFF LOCAL :OFF
FADER :Mittelage T-ADV :OFF AUTO :OFF
BASS :Mittelage METAL :OFF
TREBLE :Mittelage DOLBY NR :OFF

NR.	GEGENSTAND	EINGANGS-EINSTELLUNG	AUSGANGS-EINSTELLUNG	TUNER(RECEIVER)-EINSTELLUNG	ABGLEICH PUNKTE	ABGLEICHEN FÜR	ABB.
UKW-ABTEILUNG							
1	DISKRIMINATOR	(A) 98,1MHz 0 Hub 60dB(ANT-Eingang)	Den Gleichstrom-Voltmeter zwischen den beiden Stiften von TP1 anschließen.	FM 98,1MHz	T1 (X11)	0V	(a)
2	VCO	(A) 98,1MHz 0 Hub 60dB(ANT-Eingang)	Den Frequenzzähler an die Stifte von TP2 anschließen.	FM 98,1MHz	VR5 (X11)	19,00kHz	(b)
3	STEREO KANAL TRENNUNG	(C) 98,1MHz 1kHz.±67,5kHz Hub Wahler:L oder R 60dB(ANT-Eingang)	(B)	FM 98,1MHz	VR4 (X11)	So einstellen, das das übersprechen von L auf R und von R auf L minimal wird.	(c)
4	HALT PEGEL	(A) 98,1MHz 0 Hub 20dB(ANT-Eingang)	-	FM 98,1MHz	VR3 (X11)	HALT	(d)
5	Weiche Dämpfung (1)	(C) 98,1MHz 1kHz.±75kHz Hub 60dB(ANT-Eingang)	(B)	FM 98,1MHz	-	Die Lautstarek auf 0 dBs einstellen.	
6	Weiche Dämpfung (2)	Die Antenne öffnen (Kein Signal).	(B)	FM 98,1MHz	VR1	- 25dBs	(f)
MW-ABTEILUNG							
(1)	HALT PEGEL	(D) 990kHz 400Hz. 30% mod 38dB(ANT-Eingang)	-	MW 990kHz	VR1 (X14)	HALT	(g)
CASSETTEN-DECK-ABTEILUNG							
[1]	AZIMUTH	MTT-114(10kHz)	(B)	Bandwiedergabe	Kopfazimutschraube	So einstellen, das das Azimuth für jeweils L-CH/R-CH oder FOW/REV maximal wird.	(h)



PC BOARD





Kenwood follows a policy of continuous advancements in development. For this reason specifications may be changed without notice. DOLBY and the double-D symbol are trademarks of Dolby Laboratories Licensing Corporation. Noise reduction circuit made under license from Dolby Laboratories Licensing Corporation.

Kenwood poursuit une politique de progrès constants en ce qui concerne le développement. Pour cette raison, les spécifications sont sujettes à modifications sans préavis. La marque DOLBY et le double "D" sont des marques déposées des Dolby Laboratories. Le système de réduction du bruit de fond est fabriqué sous licence des Dolby Laboratories.

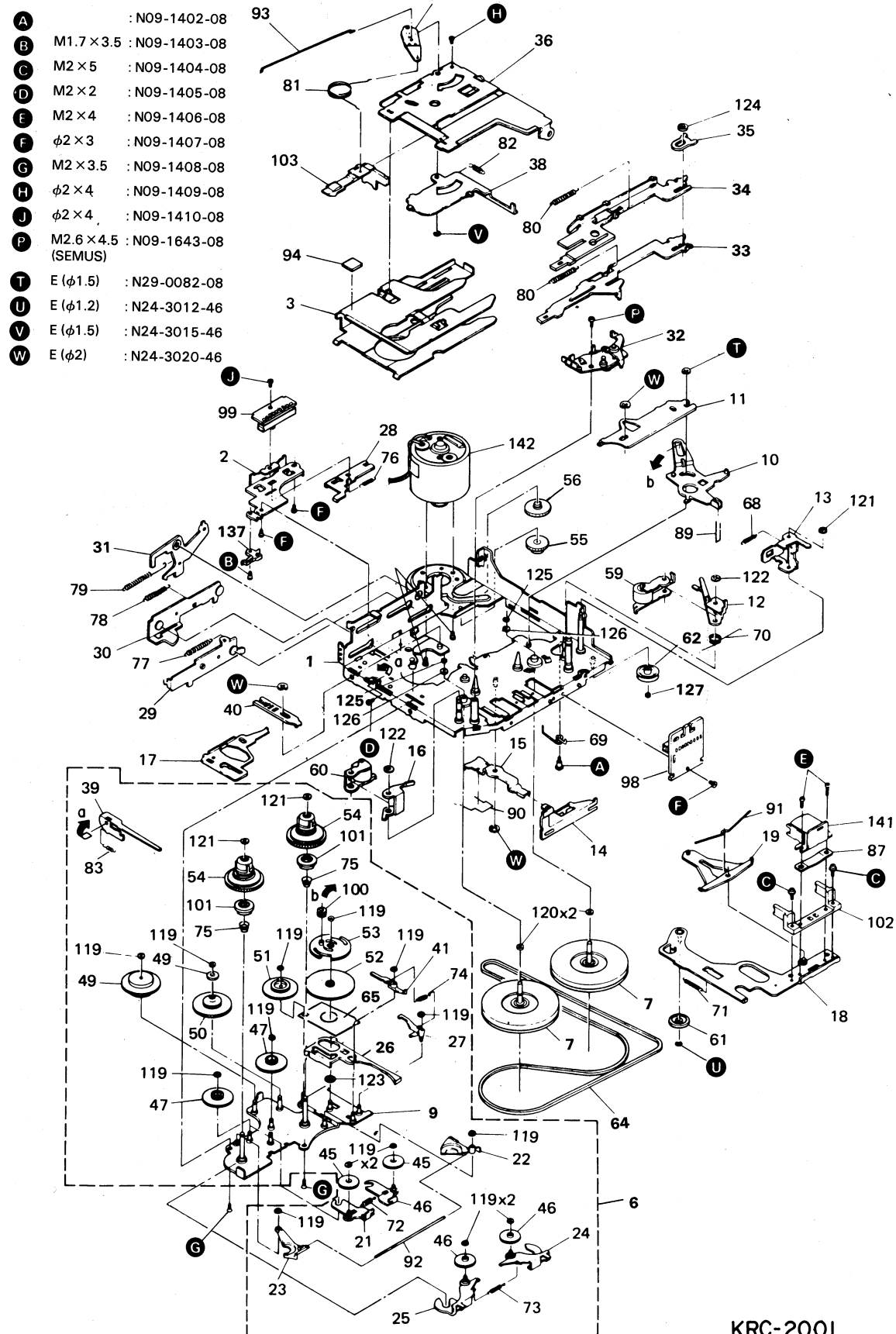
Kenwood strebt ständige Verbesserungen in der Entwicklung an. Daher bleiben Änderungen der technischen Daten jederzeit vorbehalten. DOLBY und Doppel-D-Symbol sind eingetragene Warenzeichen der Dolby Laboratories. Dolby-Rauschunterdrückung mit Lizenz der Dolby Laboratories gefertigt.

- DC voltages are as measured with a high impedance voltmeter. Values may vary slightly due to variations between individual instruments or/and units.
- Les tensions c.c. doivent être mesurées avec un voltmètre à haute impédance. Les valeurs peuvent différer légèrement du fait des variations inhérentes aux appareils et aux instruments de mesure individuels.
- Die angegebenen Gleichspannungswerte wurden mit einem hochohmigen Voltmeter gemessen. Dabei schwanken die Meßwerte aufgrund von Unterschieden zwischen einzelnen Instrumenten oder Geräten u.U. geringfügig.

KRC-2001 (K)

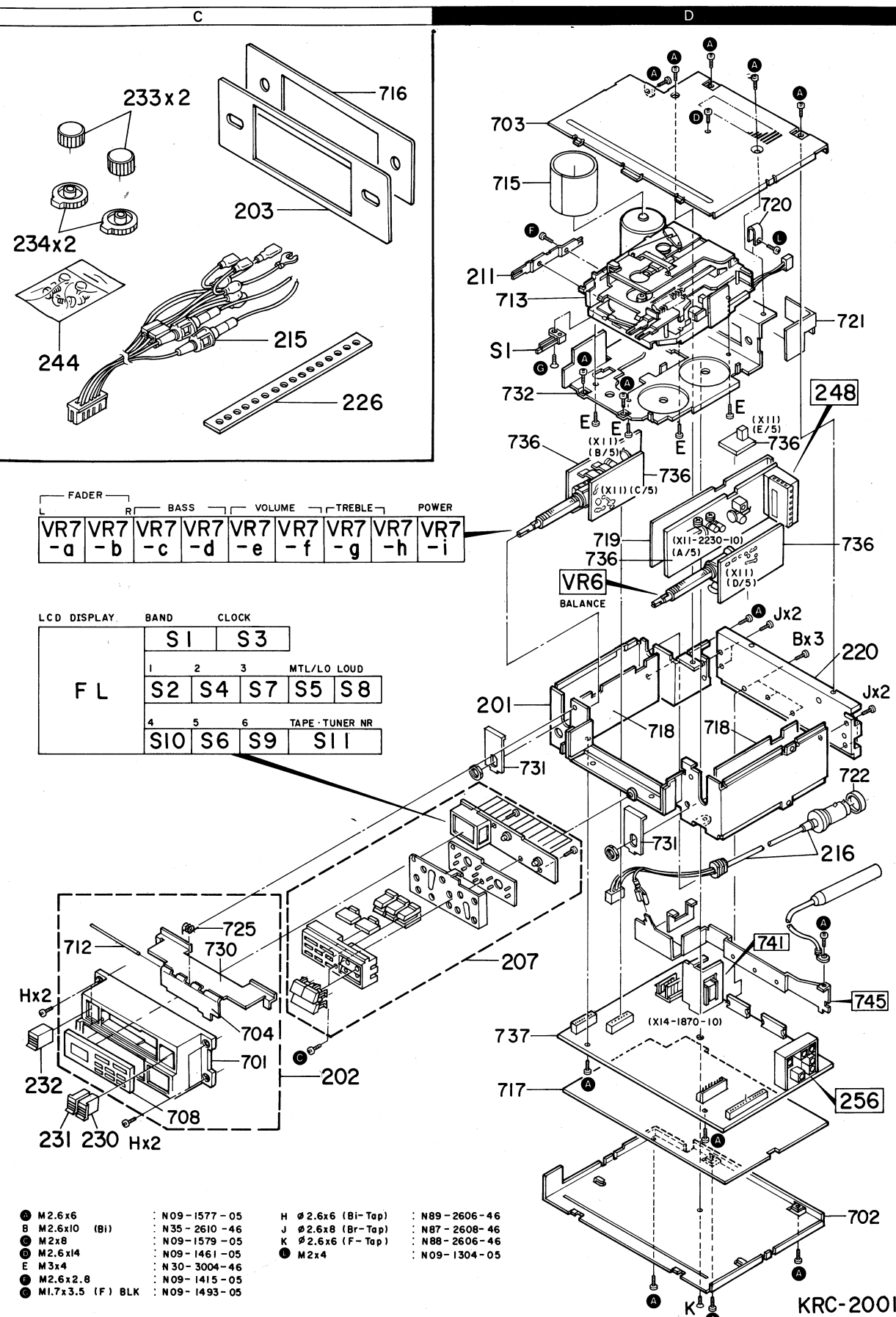
KRC-2001
KENWOOD

EXPLODED VIEW (MECHANISM UNIT)



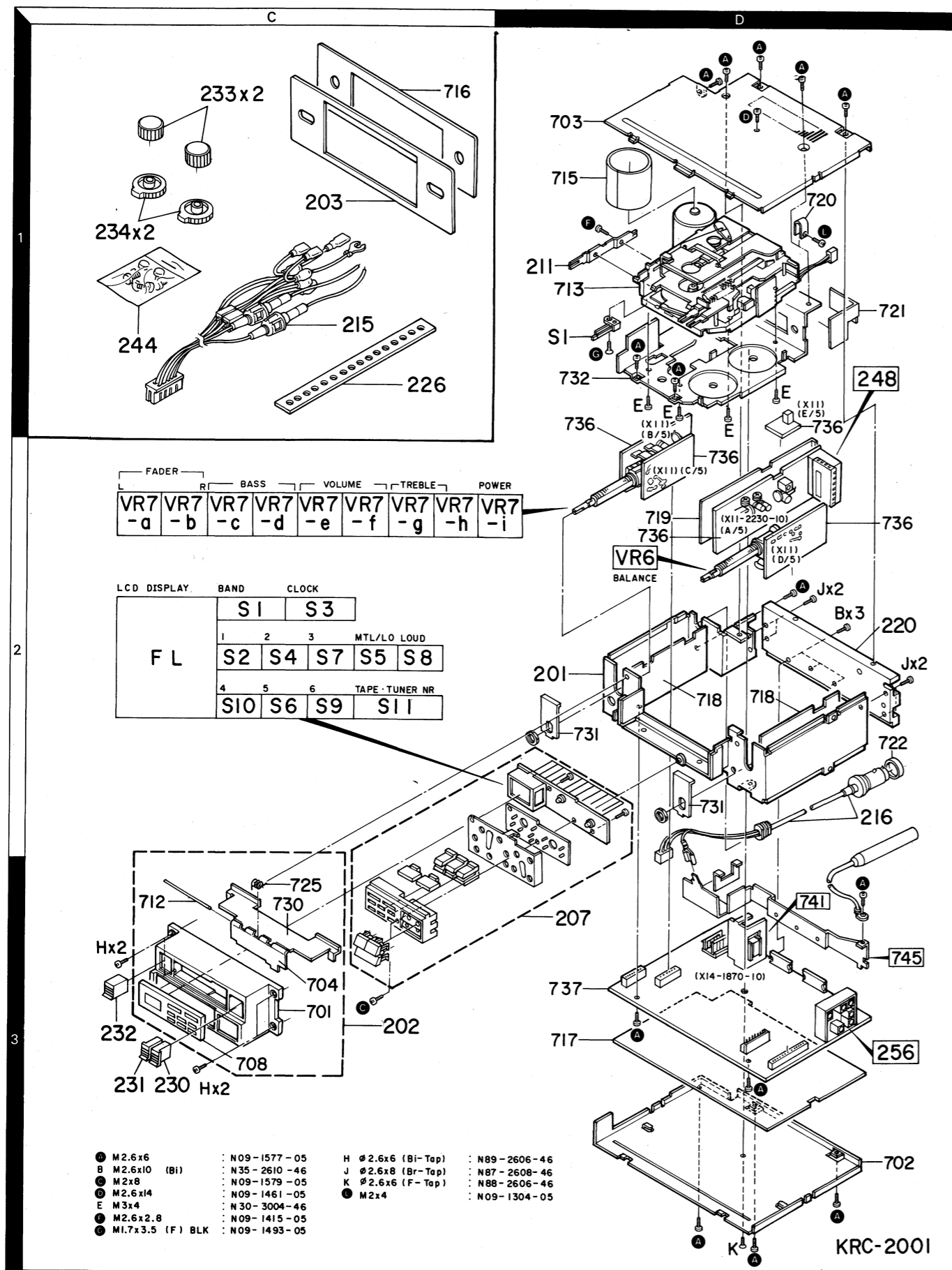
Parts with the exploded numbers larger than 700 are not supplied.

EXPLODED VIEW (MAIN UNIT)



KRC-2001 KRC-2001

EXPLODED VIEW (MAIN UNIT)



PARTS LIST

✕ New Parts

Parts without **Parts No.** are not supplied.

Les articles non mentionnés dans le **Parts No.** ne sont pas fournis.

Telle ohne **Parts No.** werden nicht geliefert.

Ref. No. 参照番号	Address 位置	New Parts 新	Parts No. 部品番号	Description 部品名 / 規格	Desti- nation 仕向	Re- marks 備考
KRC-2001						
201	2D	*	A01-1464-02	METALLIC CABINET		
202	3C	*	A20-4735-02	PANEL ASSY		
203	1C	*	A21-0493-03	DRESSING PANEL		
207	3D	*	B38-0066-05	LCD ASSY		
-			B46-0100-00	WARRANTY CARD		
-			B46-0118-03	QUESTIONNAIRE CARD		
-		*	B50-6065-00	INSTRUCTION MANUAL		
-		*	B58-0376-04	CAUTION CARD		
211	1D		D10-1318-04	LEVER		
215	1C	*	E30-1430-15	CORD WITH CONNECTOR		
216	2D	*	E30-1431-05	CORD WITH DIN CONNECTOR		
220	2D	*	F01-0681-03	HEAT SINK		
-		*	H01-7079-04	ITEM CARTON CASE		
-		*	H03-0797-04	OUTER CARTON CASE		
-		*	H10-1860-03	POLYSTYRENE FOAMED FIXTURE		
-		*	H10-1878-03	POLYSTYRENE FOAMED FIXTURE		
-			H25-0029-04	PROTECTION BAG (60X110)		
-			H25-0117-04	PROTECTION BAG (80X250X0.07)		
-			H25-0148-04	PROTECTION BAG (110X230X0.07)		
-			H25-0173-04	PROTECTION BAG (300X350X0.05)		
-			H25-0226-04	PROTECTION BAG (180X300X0.05)		
-			H25-0234-04	PROTECTION BAG		
226	1C		J54-0059-04	STAY		
230	3C	*	K27-1579-04	KNOB (BUTTON) FF		
231	3C	*	K27-1580-04	KNOB (BUTTON) REW		
232	3C	*	K27-1581-04	KNOB (BUTTON) EJECT		
233	1C		K29-1888-04	KNOB		
234	1C		K29-1889-04	KNOB		
244	1C		N99-0066-15	SCREW SET		
A	1D, 2D		N09-1577-05	TAPTITE SCREW (M2.6X6)		
C	3C	*	N09-1579-05	TAPTITE SCREW (M2X8)		
D	1D		N09-1461-05	STEPPED SCREW (M2.6X14)		
F	1D		N09-1415-05	MACHINE SCREW (M2.6X2.8)		
G	1D		N09-1493-05	MACHINE SCREW (M1.7X3.5)		
L	1D	*	N09-1304-05	TAPPING SCREW (2X4)		
S1	1D		S46-1081-05	LEAF SWITCH		
CONTROL UNIT (X11-2230-10)						
C1		*	CE04DW1C100M	ELECTRQ	10UF	16WV
C2 ,3			CE04DW1A470M	ELECTRQ	47UF	10WV
C4			CE04DW1HR47M	ELECTRQ	0.47UF	50WV
C5 --9			C93-0012-05	CYLND CHIP C	0.01UF	M
C10			C90-0484-05	ELECTRQ	0.47UF	50WV
C11			CE04DW1C100M	ELECTRQ	10UF	16WV
C12			CK41DB1H221K	CYLND CHIP C	220PF	K
C13		*	CE04DW1A470M	ELECTRQ	47UF	10WV
C14			CE04DW1HR47M	ELECTRQ	0.47UF	50WV
C15			C93-0012-05	CYLND CHIP C	0.01UF	M
C16			C90-0482-05	ELECTRQ	4.7UF	25WV
C17			C90-0824-05	ELECTRQ	1UF	50WV

E: Scandinavia & Europe H: Audio Club K: USA

P: Canada

S: South Africa T: England U: PX(Far East, Hawaii)

UE : AAFES(Europe) **X**: Australia **M**: Other Areas

 indicates safety critical components.

supplied.

PARTS LIST

* New Parts

Parts without Parts No. are not supplied.

Les articles non mentionnés dans le Parts No. ne sont pas fournis.

Teile ohne Parts No. werden nicht geliefert.

Ref. No. 参照番号	Address 位置	New Parts 新	Parts No. 部品番号	Description 部品名 / 規格	Desti- nation 仕向	Re- marks 備考
C18 ,19			C93-0012-05	CYLND CHIP C 0.01UF M		
C20			CC41DSL1H330J	CYLND CHIP C 33PF J		
C21			CE04DW1A101M	ELECTR0 100UF 10WV		
C22			CE04DW1C100M	ELECTR0 10UF 16WV		
C23			CQ92M1H153J	MYLAR 0.015UF J		
C24 -27			C93-0012-05	CYLND CHIP C 0.01UF M		
C28			CE04DW1H010M	ELECTR0 1.0UF 50WV		
C29			C93-0012-05	CYLND CHIP C 0.01UF M		
C30		*	CE04DW1E4R7M	ELECTR0 4.7UF 25WV		
C31			CQ92P2A102J	MYLAR 1000PF J		
C32			CE04DW1H010M	ELECTR0 1.0UF 50WV		
C33		*	CE04DW1E3R3M	ELECTR0 3.3UF 25WV		
C34			CE04DW1H2R2M	ELECTR0 2.2UF 50WV		
C35 ,36			C93-0004-05	CYLND CHIP C 2200PF M		
C37			C90-0822-05	ELECTR0 47UF 16WV		
C38			CE04DW1H010M	ELECTR0 1.0UF 50WV		
C39			CQ92M1H333J	MYLAR 0.033UF J		
C40			CK41DB1H101K	CYLND CHIP C 100PF K		
C41			CE04DW1C100M	ELECTR0 10UF 16WV		
C42			CK41DB1H681K	CYLND CHIP C 680PF K		
C43			C90-0822-05	ELECTR0 47UF 16WV		
C44 ,45			CE04DW1C100M	ELECTR0 10UF 16WV		
C46 ,47			CK41DB1H221K	CYLND CHIP C 220PF K		
C48 ,49			CE04DW1H010M	ELECTR0 1.0UF 50WV		
C50 ,51			CE04DW1A101M	ELECTR0 100UF 10WV		
C52 ,53			CE04DW1HR47M	ELECTR0 0.47UF 50WV		
C54 ,55		*	CE04DW1E4R7M	ELECTR0 4.7UF 25WV		
C56 ,57		*	CE04DW1A470M	ELECTR0 47UF 10WV		
C58 -61		*	CE04DW1E4R7M	ELECTR0 4.7UF 25WV		
C62 ,63		*	C93-0001-05	CYLND CHIP C 1200PF M		
C64			C90-0482-05	ELECTR0 4.7UF 25WV		
C65		*	CE04DW1E4R7M	ELECTR0 4.7UF 25WV		
C66 ,67			CE04DW1A101M	ELECTR0 100UF 10WV		
C68 ,69			CQ92M1H223J	MYLAR 0.022UF J		
C70		*	CE04DW1E4R7M	ELECTR0 4.7UF 25WV		
C71			C90-0482-05	ELECTR0 4.7UF 25WV		
C72 ,73			CQ92M1H223J	MYLAR 0.022UF J		
C74		*	CE04DW1E4R7M	ELECTR0 4.7UF 25WV		
C75			C90-0482-05	ELECTR0 4.7UF 25WV		
C76 ,77			CE04DW1C100M	ELECTR0 10UF 16WV		
C78 -80			C93-0012-05	CYLND CHIP C 0.01UF M		
C81		*	CE04DW1A470M	ELECTR0 47UF 10WV		
CF1 ,2			L72-0170-05	CERAMIC FILTER		
L1			L33-0291-05	CHOKE COIL		
T1			L30-0395-05	FM IFT		
J1 -6			R92-0338-05	CLYND CHIP R		
J8 -13			R92-0338-05	CLYND CHIP R		
J16 -19			R92-0338-05	CLYND CHIP R		
R1			RD41DB2B473J	CYLND CHIP R 47K J 1/8W		
R2			RD41DB2B102J	CYLND CHIP R 1.0K J 1/8W		
R3 ,4			RD41DB2B223J	CYLND CHIP R 22K J 1/8W		
R5			RD41DB2B100J	CYLND CHIP R 10 J 1/8W		
R7			RD41DB2B561J	CYLND CHIP R 560 J 1/8W		
R8			RD41DB2B332J	CYLND CHIP R 3.3K J 1/8W		

E: Scandinavia & Europe H: Audio Club K: USA

P: Canada

S: South Africa

T: England

U: PX(Far East, Hawaii)

UE: AAFES(Europe)

X: Australia

M: Other Areas

⚠ indicates safety critical components.

PARTS LIST

× New Parts

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Ref. No. 参照番号	Address 位置	New Parts 新	Parts No. 部品番号	Description 部品名 / 規格	Desti- nation 仕 向	Re- marks 備考
R9			RD41DB2B561J	CYLND CHIP R 560 J 1/8W		
R10			RD41DB2B470J	CYLND CHIP R 47 J 1/8W		
R11			RD41DB2B331J	CYLND CHIP R 330 J 1/8W		
R12			RD41DB2B220J	CYLND CHIP R 22 J 1/8W		
R13			RD41DB2B331J	CYLND CHIP R 330 J 1/8W		
R14			RD41DB2B103J	CYLND CHIP R 10K J 1/8W		
R15			RD41DB2B123J	CYLND CHIP R 12K J 1/8W		
R17			RD41DB2B473J	CYLND CHIP R 47K J 1/8W		
R18			RD41DB2B683J	CYLND CHIP R 68K J 1/8W		
R20			RD41DB2B102J	CYLND CHIP R 1.0K J 1/8W		
R21			RD41DB2B183J	CYLND CHIP R 18K J 1/8W		
R22			RD41DB2B103J	CYLND CHIP R 10K J 1/8W		
R23			RD41DB2B102J	CYLND CHIP R 1.0K J 1/8W		
R24			RD41DB2B123J	CYLND CHIP R 12K J 1/8W		
R26			RD41DB2B102J	CYLND CHIP R 1.0K J 1/8W		
R27			RD41DB2B123J	CYLND CHIP R 12K J 1/8W		
R28			RD41DB2B222J	CYLND CHIP R 2.2K J 1/8W		
R29			RD41DB2B223J	CYLND CHIP R 22K J 1/8W		
R30			RD41DB2B222J	CYLND CHIP R 2.2K J 1/8W		
R31			RD41DB2B102J	CYLND CHIP R 1.0K J 1/8W		
R32 ,33			RD41DB2B123J	CYLND CHIP R 12K J 1/8W		
R34			RD41DB2B222J	CYLND CHIP R 2.2K J 1/8W		
R35			RD41DB2B473J	CYLND CHIP R 47K J 1/8W		
R36			RD41DB2B102J	CYLND CHIP R 1.0K J 1/8W		
R37		*	RD41DB2B274J	CYLND CHIP R 270K J 1/8W		
R38 ,39			RD41DB2B103J	CYLND CHIP R 10K J 1/8W		
R40			RD41DB2B563J	CYLND CHIP R 56K J 1/8W		
R41			RD41DB2B473J	CYLND CHIP R 47K J 1/8W		
R42			RD41DB2B103J	CYLND CHIP R 10K J 1/8W		
R43			RD41DB2B563J	CYLND CHIP R 56K J 1/8W		
R44			RD41DB2B473J	CYLND CHIP R 47K J 1/8W		
R45 -47			RD41DB2B103J	CYLND CHIP R 10K J 1/8W		
R48			RD41DB2B121J	CYLND CHIP R 120 J 1/8W		
R49			RD41DB2B223J	CYLND CHIP R 22K J 1/8W		
R51			RD41DB2B223J	CYLND CHIP R 22K J 1/8W		
R52			RD41DB2B512J	CYLND CHIP R 5.1K J 1/8W		
R53			RD41DB2B103J	CYLND CHIP R 10K J 1/8W		
R54		*	RD41DB2B391J	CYLND CHIP R 390 J 1/8W		
R55			RD41DB2B243J	CYLND CHIP R 24K J 1/8W		
R56 ,57			RD41DB2B122J	CYLND CHIP R 1.2K J 1/8W		
R58 ,59			RD41DB2B102J	CYLND CHIP R 1.0K J 1/8W		
R60			RD41DB2B182J	CYLND CHIP R 1.8K J 1/8W		
R61 ,62			RD41DB2B153J	CYLND CHIP R 15K J 1/8W		
R63			RD41DB2B103J	CYLND CHIP R 10K J 1/8W		
R64			RD41DB2B182J	CYLND CHIP R 1.8K J 1/8W		
R65			RD41DB2B103J	CYLND CHIP R 10K J 1/8W		
R66 ,67			RD41DB2B222J	CYLND CHIP R 2.2K J 1/8W		
R68			RD41DB2B101J	CYLND CHIP R 100 J 1/8W		
R69			RD41DB2B183J	CYLND CHIP R 18K J 1/8W		
R70 ,71			RD41DB2B102J	CYLND CHIP R 1.0K J 1/8W		
R72 ,73			RD41DB2B222J	CYLND CHIP R 2.2K J 1/8W		
R74			RD41DB2B223J	CYLND CHIP R 22K J 1/8W		
R75			RD41DB2B101J	CYLND CHIP R 100 J 1/8W		
R76 ,77			RD41DB2B561J	CYLND CHIP R 560 J 1/8W		
R79			RD41DB2B103J	CYLND CHIP R 10K J 1/8W		

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R80 R81 .82 R83 .84 R85 .86 R87 .88		*	RD14DB2H4R7J RD41DB2B563J RD41DB2B224J RD41DB2B470J RD41DB2B682J	SMALL-RD 4.7 J 1/2W CYLND CHIP R 56K J 1/8W CYLND CHIP R 220K J 1/8W CYLND CHIP R 47 J 1/8W CYLND CHIP R 6.8K J 1/8W		
R89 .90 R91 R92 R93 .94 R96			RD41DB2B154J RD41DB2B103J RD41DB2B473J RD41DB2B104J RD41DB2B473J	CYLND CHIP R 150K J 1/8W CYLND CHIP R 10K J 1/8W CYLND CHIP R 47K J 1/8W CYLND CHIP R 100K J 1/8W CYLND CHIP R 47K J 1/8W		
R97 R98 R99 R100 R101,102			RD41DB2B104J RD41DB2B223J RD41DB2B473J RD41DB2B101J RD41DB2B222J	CYLND CHIP R 100K J 1/8W CYLND CHIP R 22K J 1/8W CYLND CHIP R 47K J 1/8W CYLND CHIP R 100 J 1/8W CYLND CHIP R 2.2K J 1/8W		
R103,104 R105,106 VR1 VR3 .4 VR5			RD41DB2B152J RD41DB2B122J R12-3103-05 R12-3101-05 R12-3100-05	CYLND CHIP R 1.5K J 1/8W CYLND CHIP R 1.2K J 1/8W TRIMMING PNT. (47K)SOFT MUTE TRIMMING PNT. (22K)STOP LVL,SEP TRIMMING PNT. (10K)VCO		
VR6 VR7	2D 2C	*	R29-3020-05 R24-3008-05	POTENTIOMETER(BALANCE) POTENTIOMETER(FADER,TREB,PWR)		
D1 -8 D9 D10 IC1 IC2		*	RLS-73 RD6.8JS(B) DSM1A1 LA1140 LA3370	DIODE ZENER DIODE DIODE IC(FM IF/DETECTION) IC(FM MPX)		
IC3 IC4 IC5 IC6 IC7			AN7381 AN6555 AN6556 KC-819 BA3406L	IC(TONE CONTROL X2) IC(OP AMP X2) IC(OP AMP X2) IC(TONE AMP X2) IC(PREAMP FOR TAPE EQ X2)		
Q1 Q2 Q3 .4			2SC2412K 2SC2413K 2SC2412K	TRANSISTOR TRANSISTOR TRANSISTOR		
248	1D	*	W02-0680-05	FM FRONT-END ASSY		
SYNTHESIZER UNIT (X14-1870-10)						
C1 C2 C3 C4 C5		*	CC41DSL1H300J CE04DW1C100M C93-0012-05 CE04DW1C470M CE04DW1C100M	CYLND CHIP C 30PF J ELECTR0 10UF 16WV CYLND CHIP C 0.01UF M ELECTR0 47UF 16WV ELECTR0 10UF 16WV		
C6 C7 C8 C9 C10			CE04DW1H010M CE04DW1A221M CE04DW1HR47M CE04DW1C100M CE04DW1A470M	ELECTR0 1.0UF 50WV ELECTR0 220UF 10WV ELECTR0 0.47UF 50WV ELECTR0 10UF 16WV ELECTR0 47UF 10WV		
C11 C12 C13 C14 .15 C16		*	C93-0012-05 CE04DW1C100M CE04DW1H010M CE04DW1E4R7M CE04DW1C100M	CYLND CHIP C 0.01UF M ELECTR0 10UF 16WV ELECTR0 1.0UF 50WV ELECTR0 4.7UF 25WV ELECTR0 10UF 16WV		
C17		*	CE04DW1A470M	ELECTR0 47UF 10WV		

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C18			CE04DW1C100M	ELECTR0 10UF 16WV		
C19			CE04DW1A221M	ELECTR0 220UF 10WV		
C20			CE04DW1C100M	ELECTR0 10UF 16WV		
C21		*	CE04DW1A470M	ELECTR0 47UF 10WV		
C22 ,23			CE04DW1H010M	ELECTR0 1.0UF 50WV		
C24 ,25			C93-0004-05	CYLND CHIP C 2200PF M		
C26 ,27			C93-0012-05	CYLND CHIP C 0.01UF M		
C28 ,29			CE04DW1A101M	ELECTR0 100UF 10WV		
C30 -33			CF92V1H474J	MF 0.47UF J		
C34 ,35			CE04DW1C102M	ELECTR0 1000UF 16WV		
C36 ,37			C93-0012-05	CYLND CHIP C 0.01UF M		
C38 ,39			CE04DW1A101M	ELECTR0 100UF 10WV		
C40 ,41			CC41DSL1H220J	CYLND CHIP C 22PF J		
C42			C93-0012-05	CYLND CHIP C 0.01UF M		
C43			CE04DW1H2R2M	ELECTR0 2.2UF 50WV		
C44			CE04DW1A101M	ELECTR0 100UF 10WV		
C45			CE04DW1A221M	ELECTR0 220UF 10WV		
C46 ,47		*	CE04DW1A470M	ELECTR0 47UF 10WV		
C48 ,49			C93-0012-05	CYLND CHIP C 0.01UF M		
C50			CE04DW1C471M	ELECTR0 470UF 16WV		
C51			C93-0012-05	CYLND CHIP C 0.01UF M		
C52			CE04DW1C102M	ELECTR0 1000UF 16WV		
C53 -57			C93-0012-05	CYLND CHIP C 0.01UF M		
C58 ,59			CE04DW1A221M	ELECTR0 220UF 10WV		
C60			C90-0478-05	ELECTR0 10UF 16WV		
C61			C91-0699-05	CERAMIC 0.1UF K		
W3		*	E30-1429-05	CARD WITH PLUG		
-			J61-0067-05	WIRE BAND		
L1			L39-0129-05	TRAP CNIL		
L2		*	L15-0035-05	LOW-FREQUENCY CHOKER CNIL		
X1			L77-0585-05	CRYSTAL RESONATOR(4.5MHZ)		
A	3D	*	N09-1577-05	TAPTITE SCREW (M2.6X6)		
CP1			R90-0450-05	MULTIPLE RESISTOR		
J1 -5			R92-0338-05	CYLND CHIP R		
J7 -44			R92-0338-05	CYLND CHIP R		
R1 ,2			RD41DB2B103J	CYLND CHIP R 10K J 1/8W		
R3			RD41DB2B102J	CYLND CHIP R 1.0K J 1/8W		
R4			RD41DB2B103J	CYLND CHIP R 10K J 1/8W		
R5			RD41DB2B102J	CYLND CHIP R 1.0K J 1/8W		
R6			RD41DB2B100J	CYLND CHIP R 10 J 1/8W		
R8			RD41DB2B473J	CYLND CHIP R 47K J 1/8W		
R9			RD41DB2B104J	CYLND CHIP R 100K J 1/8W		
R10			RD41DB2B333J	CYLND CHIP R 33K J 1/8W		
R11 ,12			RD41DB2B103J	CYLND CHIP R 10K J 1/8W		
R13			RD41DB2B473J	CYLND CHIP R 47K J 1/8W		
R14			RD41DB2B823J	CYLND CHIP R 82K J 1/8W		
R15			RD41DB2B103J	CYLND CHIP R 10K J 1/8W		
R16 ,17			RD41DB2B153J	CYLND CHIP R 15K J 1/8W		
R18			RD41DB2B223J	CYLND CHIP R 22K J 1/8W		
R19 -21			RD41DB2B473J	CYLND CHIP R 47K J 1/8W		
R22 -24			RD41DB2B472J	CYLND CHIP R 4.7K J 1/8W		
R25			RD41DB2B102J	CYLND CHIP R 1.0K J 1/8W		

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R26 .27			RD41DB2B473J	CYLND CHIP R 47K J 1/8W		
R28			RD41DB2B103J	CYLND CHIP R 10K J 1/8W		
R29		*	RD41DB2B271J	CYLND CHIP R 270 J 1/8W		
R31			RD41DB2B222J	CYLND CHIP R 2.2K J 1/8W		
R32			RD41DB2B473J	CYLND CHIP R 47K J 1/8W		
R34			RD41DB2B153J	CYLND CHIP R 15K J 1/8W		
R35 .36			RD41DB2B152J	CYLND CHIP R 1.5K J 1/8W		
R37 -40		*	RD41DB2B4R7J	CYLND CHIP R 4.7 J 1/8W		
R41 -49			RD41DB2B103J	CYLND CHIP R 10K J 1/8W		
R50 -57			RD41DB2B104J	CYLND CHIP R 100K J 1/8W		
R58			RD41DB2B103J	CYLND CHIP R 10K J 1/8W		
R59			RD41DB2B473J	CYLND CHIP R 47K J 1/8W		
R60			RD41DB2B223J	CYLND CHIP R 22K J 1/8W		
R61 .62			RD41DB2B103J	CYLND CHIP R 10K J 1/8W		
R64			RD41DB2B682J	CYLND CHIP R 6.8K J 1/8W		
R65			RD41DB2B152J	CYLND CHIP R 1.5K J 1/8W		
R66			RD41DB2B102J	CYLND CHIP R 1.0K J 1/8W		
R67			RD41DB2B182J	CYLND CHIP R 1.8K J 1/8W		
R68			RD41DB2B103J	CYLND CHIP R 10K J 1/8W		
R69			RD41DB2B101J	CYLND CHIP R 100 J 1/8W		
R70			RD41DB2B332J	CYLND CHIP R 3.3K J 1/8W		
R71			RD41DB2B100J	CYLND CHIP R 10 J 1/8W		
R72			RD41DB2B102J	CYLND CHIP R 1.0K J 1/8W		
R73			RD41DB2B103J	CYLND CHIP R 10K J 1/8W		
R74			RD41DB2B473J	CYLND CHIP R 47K J 1/8W		
VR1			R12-3096-05	TRIMMING PNT. (10K) AM, STMP LVL		
D1 -15			RLS-73	DIODE		
D16			RD10JS(B)	ZENER DIODE		
D17 .18			RLS-73	DIODE		
D19 -22			1S553	DIODE		
D19 -22			1S1555	DIODE		
D19 -22			1S2076	DIODE		
D23 -28			RLS-73	DIODE		
D29 -32			1S553	DIODE		
D29 -32			1S1555	DIODE		
D29 -32			1S2076	DIODE		
D33 -38			RLS-73	DIODE		
D39			DSM1A1	DIODE		
D40			V03C	DIODE		
D41			RD6.2JS(B)	ZENER DIODE		
D42		*	DSP-301NF	SURGE ABSORBER		
D43			RLS-73	DIODE		
D44			RD5.1JS(B)	ZENER DIODE		
IC1	IC270B	*	UPD1708G-637-00	IC(PLL FREQ SYNTHESISER CONTR)		
IC2			LA4460	IC(AF POWER AMP/ 12W)		
IC3			LA4461	IC(AF POWER AMP/ 12W)		
IC4 .5			TC4081BP	IC(AND X4)		
IC4 .5			UPD4081BC	IC(AND X4)		
Q1 -8			2SC2412K	TRANSISTOR		
Q9			2SA1036K	TRANSISTOR		
Q10			2SC2411K	TRANSISTOR		
Q11			2SC2412K	TRANSISTOR		
Q12			2SA1020	TRANSISTOR		
Q13 -15			2SC2412K	TRANSISTOR		

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Q16 Q17 Q18 ,19 Q18 ,19 Q20 ,21			2SC2411K 2SA1037K 2SD1328 2SD1757K DTA144EK	TRANSISTOR TRANSISTOR TRANSISTOR TRANSISTOR DIGITAL TRANSISTOR		
Q22 -24 Q25			DTC124EK DTA144EK	DIGITAL TRANSISTOR DIGITAL TRANSISTOR		
256	3D	*	W02-0681-05	TUNER ASSY		
D1 -3 D1 -3 FET1 FET2 FET2			SVC211 1SV103 3SK101 2SK302 2SK360	DIODE DIODE FET FET FET		
TR1 ,2 TR1 ,2			2SC2620 2SC2714	TRANSISTOR TRANSISTOR		
- - D1 -3 D1 -3 TR1 -3			2SK163 2SK523 SVC321 1SV149 2SC2619	FET FET DIODE DIODE TRANSISTOR		
TR1 -3 TR1 -3			2SC2716 2SC2814	TRANSISTOR TRANSISTOR		
- - - - -			N09-0334-05 N09-0335-05 N09-0366-05 N10-1050-46 N14-0131-05	HEXAGON HEAD BOLT (M5X8) TAPPING SCREW HEXAGON HEAD BOLT (M5X20) HEXAGON NUT NUT		
- - -			N15-1050-46 N17-2050-46 N19-0337-05	FLAT WASHER (25) TOOTHED LOCK WASHER FLAT WASHER		
CASSETTE MECHANISM ASS'Y						
1 2 3 6	2A 2A 1A 3B	*	A10-0886-08 A10-0770-08 A53-0674-08 D03-0241-08	CHASSIS ASSY CHASSIS (PM BRACKET) CASSETTE HOLDER REEL DISK ASSY		
7 9 10 11 12	3B 3A 2B 1B 2B	*	D01-0073-08 D03-0241-08 D10-1319-08 D10-1320-08 D10-1321-08	FLYWHEEL ASSY (F) MG PLATE ASSY SWITCH PLATE ASSY MAIN PLATE (M) LEVER (TS ACTUATOR)		
13 14 15 16 17	2B 2B 2B 2A 2A	*	D10-1322-08 D10-1323-08 D10-1324-08 D10-1651-08 D10-1326-08	LOCK PLATE (FR) SLIDER (FR) LEVER (FR ACTUATOR) ARM (PULL PLATE) SLIDER (TG PUSH PLATE)		
18 19 20 21 22	3B 2B 3A 3A 3B		D10-1327-08 D10-1328-08 D10-1329-08 D10-1330-08 D10-1331-08	HEAD PANEL ASSY (M) ARM (PR ACTUATOR) FG PLATE ASSY RG PLATE ASSY ARM (ED PLATE) F		

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23	3A		D10-1332-08	ARM (FD PLATE)R		
24	3B		D10-1333-08	TG PLATE (F) ASSY		
25	3A		D10-1334-08	TG PLATE (R) ASSY		
26	3A	*	D10-1335-18	PLATE (ES)		
27	3A		D10-1336-08	ARM (TRIGGER)		
28	2A		D10-1337-08	LEVER (SWITCH ACTUATOR)		
29	2A		D10-1338-08	PUSH LEVER ASSY		
30	2A		D10-1339-08	CH PUSH PLATE ASSY		
31	2A		D10-1340-08	LEVER (LIFT UP)		
32	1B	*	D10-1652-08	FR BRACKET ASSY		
33	1B	*	D10-1654-08	LEVER (REW)		
34	1B	*	D10-1653-08	LEVER (FF)		
35	1B		D10-1344-08	PC PLATE		
36	1B		D10-1345-08	CASE LIFTER		
37	1A		D10-1346-08	PE PLATE ASSY		
38	1B		D10-1347-08	CD PLATE		
39	2A		D10-1348-08	LEVER (TIMING)		
40	2A		D10-1349-08	ARM (TG ACTUATOR)		
41	3A		D10-1350-08	ARM (STOP)		
45	3A		D13-0185-08	GEAR (F)		
46	3A, 3B		D13-0186-08	GEAR (T)		
47	3A		D13-0187-08	GEAR (FT)		
48	3A		D13-0188-08	CLUTCH ASSY (FR)		
49	3A		D13-0189-08	GEAR (DEVICE)UPPER		
50	3A		D13-0190-08	GEAR (DEVICE)LOWER		
51	3A		D13-0191-08	GEAR (DT)		
52	3A		D13-0192-08	GEAR (TS ACTUATOR)		
53	3A		D13-0193-08	GEAR (TURNOVER)		
54	2A		D13-0194-08	REEL ASSY (TAKE-UP REEL ASSY)		
55	2B	*	D13-0331-08	GEAR (MAIN)		
56	2B	*	D13-0332-08	GEAR DG		
59	2B		D14-0114-08	PINCH ROLLER ASSY (F)		
60	2A		D14-0115-08	PINCH ROLLER ASSY (R)		
61	3B		D14-0116-08	IDLER (HEAD PANEL)		
62	2B	*	D15-0244-08	PULLEY (CENTER)		
64	3B	*	D16-0109-18	BELT (MAIN)		
65	3A		D16-0112-08	SLIP SHEET		
68	2B		G01-1560-08	TENSION SPRING (FR LOCK)		
69	2B		G01-1561-08	TORSION SPRING (CONTROL)		
70	2B		G01-1562-08	TORSION SPRING (TS ACTUATOR)		
71	3B		G01-1563-08	TENSION SPRING		
72	3A		G01-1564-08	TENSION SPRING (FR GEAR PLATE)		
73	3B		G01-1565-08	TENSION SPRING (TG PLATE)		
74	3A		G01-1566-08	TENSION SPRING (TS)		
75	2A, 3A		G01-1567-08	COMPRESSION SPRING (ED)		
76	2A		G01-1568-08	TENSION SPRING (PS)		
77	2A		G01-1569-08	TENSION SPRING (PUSH LEVER)		
78	2A		G01-1570-08	TENSION SPRING (CH)		
79	2A		G01-1571-08	TENSION SPRING (LIFT UP LEVER)		
80	1B		G01-1572-08	TENSION SPRING (FR LEVER)		
81	1A		G01-1573-08	TORSION SPRING (TURNOVER)		
82	1B		G01-1574-08	TENSION SPRING (CD)		
83	2A		G01-1575-08	TENSION SPRING (TIMING LEVER)		
87	2B		G02-0174-08	FLAT SPRING (P/B HEAD)		

E: Scandinavia & Europe H: Audio Club K: USA

P: Canada

S: South Africa

T: England

U: PX (Far East, Hawaii)

UE: AAFES (Europe)

X: Australia

M: Other Areas

⚠ indicates safety critical components.

PARTS LIST

× New Parts

Parts without Parts No. are not supplied.

Les articles non mentionnés dans le Parts No. ne sont pas fournis.

Teile ohne Parts No. werden nicht geliefert.

Ref. No. 参照番号	Address 位置	New Parts 新	Parts No. 部品番号	Description 部品名 / 規格	Desti- nation 仕 向	Re- marks 備考
88	3A		G02-0175-08	FLAT SPRING (ES)		
89	2B		G09-0047-08	SPRING (HS)		
90	2B		G09-0048-08	SPRING (FR ACTUATOR)		
91	2B		G09-0049-08	SPRING (PINCH ROLLER)		
92	3A		G09-0050-08	SPRING (ES PUSH LEVER)		
93	1A		G09-0051-08	SPRING (PE)		
94	1A		G13-0167-08	CUSHION		
98	2B		J25-4472-08	PRINTED WIRING BOARD (A)		
99	1A		J25-4473-08	PRINTED WIRING BOARD (B)		
100	3A		J31-0242-08	COLLAR (TURNOVER GEAR)		
101	2A,3A		J31-0243-08	COLLAR (ED PIECE)		
102	3B		J90-0149-08	GUIDE (TAPE)		
103	1A		J90-0150-08	SLIDER (PACK)		
119	3A,2B		N19-0894-08	FLAT WASHER		
120	3B		N19-0895-08	FLAT WASHER (FLYWHEEL)		
121	2A,2B		N19-0896-08	FLAT WASHER (REEL ASSY, L PLT 10)		
122	2A,2B		N19-0897-08	FLAT WASHER (PINCH ROLLER ASSY)		
123	3A		N19-0898-08	FLAT WASHER (GEAR 59)		
124	1B		N19-0899-08	FLAT WASHER (PC PLATE 91)		
125	2A,2B		N19-0942-08	FLAT WASHER		
126	2A,2B		N19-0901-08	FLAT WASHER		
127	2B	*	N19-1015-08	FLAT WASHER PULLEY 62		
A	2B		N09-1402-08	SCREW (COLLAR)		
B	2A		N09-1403-08	SCREW (M1.7X3.5) LEAF SW 81		
C	2B		N09-1404-08	SCREW (M2X5) TAPE GUIDE 31		
D	2A,2B		N09-1405-08	SCREW (M2X2) MGT. TIMING LEV 101		
E	2B		N09-1406-08	SCREW (M2X4) PLAYBACK HEAD 33		
F	2A,2B		N09-1407-08	SCREW (Ø2X3) PM BRKT 70, PCB 20		
G	3A		N09-1408-08	SCREW (M2X3.5) MG PLATE ASSY 40		
H	1B		N09-1409-08	SCREW (Ø2X4) LIFTER 93, BRKT 88		
J	1A		N09-1410-08	SCREW (Ø2X4) PCB 71		
P	1B	*	N09-1643-08	SCREW (SEMUS) FR BRKT 32		
T	1B		N29-0082-08	E TYPE RETAINING RING		
U	3B		N24-3012-46	E TYPE RETAINING RING		
V	1A		N24-3015-46	E TYPE RETAINING RING		
W	2A,1B		N24-3020-46	E TYPE RETAINING RING		
137	2A		S46-1081-05	LEAF SWITCH (MUTING)		
141	2B		T31-0026-08	PLAYBACK HEAD		
142	2B	*	T42-0090-08	MOTOR ASSY		

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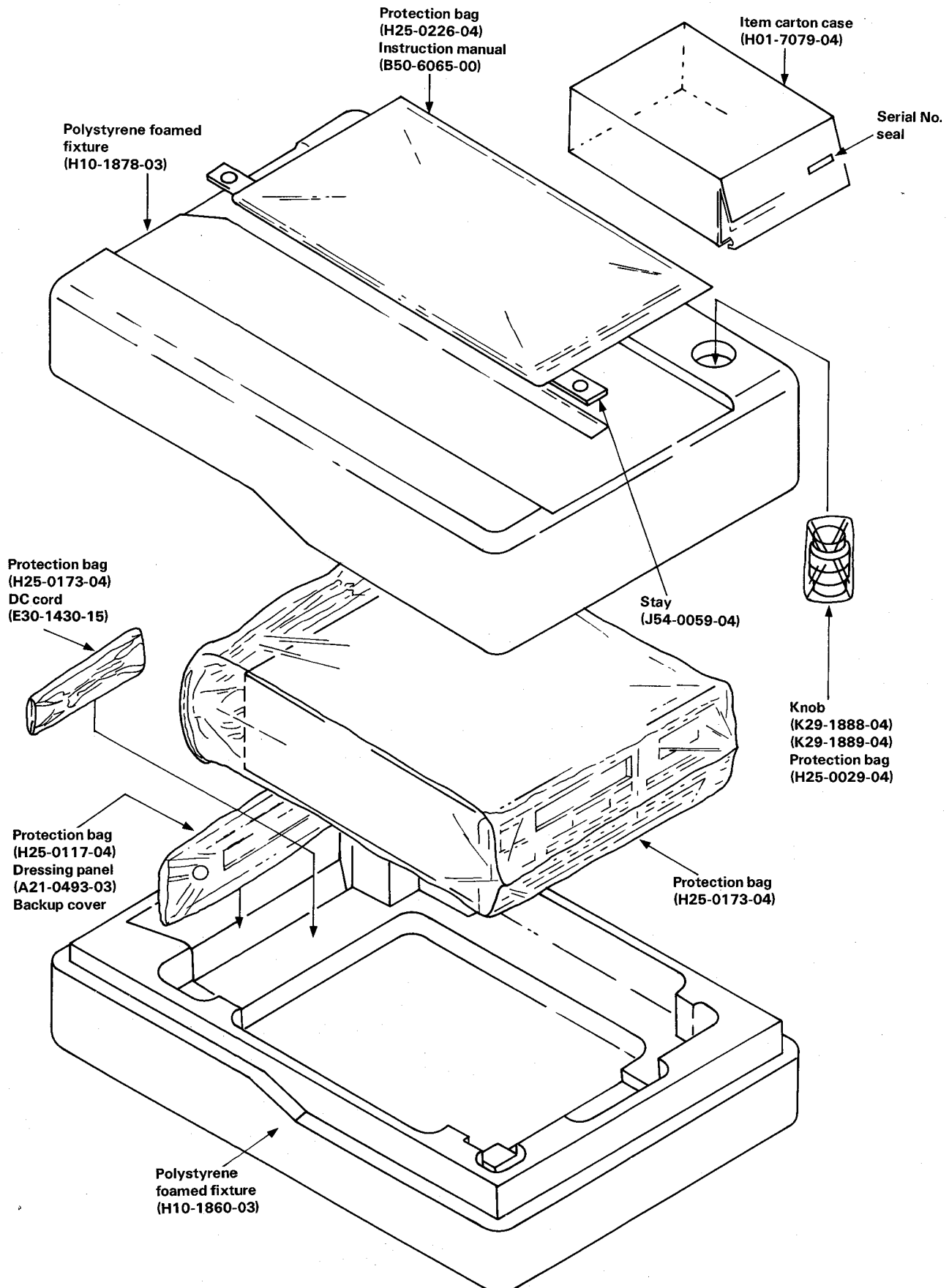
UE: AAFES (Europe)

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PACKING



Specifications

Specifications subject to change without notice.

FM Tuner Section

Frequency Range (200 kHz space)	87.9 MHz ~ 107.9 MHz
(50 kHz space)	87.5 MHz ~ 108.0 MHz
Channel Space	200 kHz/50 kHz
Usable Sensitivity	15.3 dBf (1.6 μ V/75 ohms)
50 dB Quieting Sensitivity	19.0 dBf (2.4 μ V/75 ohms)
Frequency Response (± 3 dB)	30 Hz ~ 15 kHz
Signal to Noise Ratio	66 dB
Alternate Channel Selectivity	65 dB
Capture Ratio	2.0 dB
Image Response Ratio	60 dB
IF Response Ratio	68 dB
Stereo Separation (1 kHz)	38 dB

AM Tuner Section

Frequency Range (10 kHz space)	530 kHz ~ 1,620 kHz
(9 kHz space)	522 kHz ~ 1,611 kHz
Channel Space	10 kHz/9 kHz
Usable Sensitivity (30 μ V)	30 dB

Cassette Deck Section

Tape Speed	4.76 cm/sec
Wow & Flutter	0.12% (WRMS)
Fast Winding Time (C-60)	110 sec
Frequency Response (120 μ s)	40 Hz ~ 14 kHz (± 3 dB)
(70 μ)	40 Hz ~ 16 kHz (± 3 dB)
Stereo Separation (1 kHz)	37 dB
Signal to Noise Ratio (NR OFF)	53 dB
(NR ON)	59 dB

Audio Section

Max. Output Power	20 W \times 2 into 4 ohms, 1 kHz
Power Output	15 W \times 2 into 4 ohms, 1 kHz at 10% THD
	10 W \times 2 into 4 ohms, 20 Hz ~ 20 kHz at 1% THD
Tone Action	Bass: 100 Hz ± 10 dB
	Treble: 10 kHz ± 10 dB
Preamp Output	300 mV/10 k ohms Load

General

Operating Voltage	14.4 V (11 ~ 16 V allowable)
Current Consumption	4.5 A at Rated Power
Body Size	180 \times 50 \times 130 mm
	(7-1/16 \times 1-15/16 \times 5-1/8 in.)
Nose Size	105 \times 44.5 \times 32 mm
	(4-1/8 \times 1-3/4 \times 1-1/4 in.)
Weight	3.3 lb (1.5 kg)

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